Conducting research for a changing society: This is what drives us at Forschungszentrum Jülich. As a member of the Helmholtz Association, we aim to tackle the grand societal challenges of our time and conduct research into the possibilities of a digitized society, a climate-friendly energy system, and a resource-efficient economy. Work together with around 6,800 employees in one of Europe’s biggest research centres and help us to shape change!

The job will be advertised until the position has been successfully filled. You should therefore submit your application as soon as possible. We look forward to receiving your application via our Online-Recruitment-System!

Questions about the vacancy?
Get in touch with us by using our contact form. Please note that for technical reasons we cannot accept applications via email. www.fz-juelich.de

Combine your scientific interest and technical skills in the field of electrical engineering to create new knowledge for the socially and scientifically relevant areas of information, energy and bioeconomy. We offer you all of this at the Central Institute of Engineering, Electronics and Analytics - Electronic Systems (ZEA-2) with modelling, designing and developing the most innovative system solutions for science and society.

We are looking to recruit a

Digital IC Engineer

Your Job:
Our team is engaged in several research projects involving integrated circuits from signal processing for particle physics over the implementation of emerging technologies like memristive devices up to finding solutions for scalable quantum bit control systems. Regardless of the application, a complex System-on-Chip (SoC) requires digital circuits to enable the full potential of performance, functionality, and flexibility. Do you have experience with digital IC designs and want to enrich our research with your knowledge on concept, design and implementation of CMOS circuits? Then we might be looking just for you!

Specific tasks are:
• Develop concepts for digital circuits and participate in the specification process
• Understand and align requirements with internal and external project partners
• Actively design and verify digital functionality with VHDL and Verilog
• Support in the implementation of circuits in modern CMOS nodes (28 nm and below) using the Cadence develop flow
• Accompany the verification of chips in the laboratory
• Writing on scientific publications and participation in conference presentations
• Work together in an international and interdisciplinary group

Your Profile:
• University degree (Master), preferably with doctorate (PhD), in electrical engineering, physics or similar fields
• Experienced with integrated CMOS circuit design, especially digital circuits
• Proficient with digital circuit description (VHDL or Verilog) and their verification
• Ability to monitor the complete design development, including works of others
• Good English capabilities

Please apply for the position even if you do not have all the required skills and knowledge.

Our Offer:
We work on the very latest issues that impact our society and are offering you the chance to actively help in shaping the change! We support you in your work with:
• A large research campus with green spaces, offering the best possible means for networking with colleagues and pursuing sports alongside work
• Comprehensive training courses and individual opportunities for personal and professional further development
• Extensive company health management
• Ideal conditions for balancing work and private life, as well as a family-friendly corporate policy
• Full-time position with the option of slightly reduced working hours and 30 days of annual leave
• Targeted services for international employees, e.g. through our International Advisory Service

We offer you an exciting and varied role in an international and interdisciplinary working environment. The position is initially for a fixed term of 2 years, with possible long-term prospects. Salary and social benefits in conformity with the provisions of the Collective Agreement for the Civil Service (TVöD).

Forschungszentrum Jülich promotes equal opportunities and diversity in its employment relations.
We also welcome applications from disabled persons.