Conducting research for a changing society: This is what drives us at Forschungszentrum Jülich. As a member of the Helmholtz Association, we aim to tackle the grand societal challenges of our time and conduct research into the possibilities of a digitized society, a climate-friendly energy system, and a resource-efficient economy. Work together with around 6,800 employees in one of Europe’s biggest research centres and help us to shape change!

We are offering

FPGA Design Engineer

Your Job:
The ongoing development in the fields of quantum computing and neuromorphic computing inevitably requires the use of advanced FPGA solutions. Advances in medical imaging also require sophisticated data processing, making FPGAs a suitable interface between sensors and the data storage. Your experience in handling and pre-processing continuous data streams makes you well suited to extract critical features and identify relevant components. Understanding the functional and performance goals the resulting system will then be designed and tested in your responsibility. You are an engineer who has experience in simulation and hardware or FPGA design. Then we are looking for you!

Specific tasks are:
• Develop hardware architectures using IP cores utilizing serial transceivers for scientific applications
• Implementation with modern design flows (MATLAB, Simulink) as well as simulating functional blocks with VHDL and Verilog
• Outline FPGA development providing specifications, requirements, test plans and documentation
• Perform system integration (RFSoC FPGA) and implement automated test cases for complete system validation including timing performance
• Work closely with interdisciplinary teams and partners and participate in the scientific

Questions about the vacancy?
Get in touch with us by using our contact form.
Please note that for technical reasons we cannot accept applications via email.
www.fz-juelich.de
Your Profile:
- Scientific university degree (Master) or comparable degree in electrical engineering, physics or similar fields
- Familiar with digital circuit technology used for Xilinx FPGAs or similar
- Experience in partitioning, constraining of synthesis, and design optimizations with respect to FPGA capabilities
- Knowledge of SoC architecture and industry standard high-speed interfaces

Our Offer:
We work on the very latest issues that impact our society and are offering you the chance to actively help in shaping the change! We support you in your work with:
- A large research campus with green spaces, offering the best possible means for networking with colleagues and pursuing sports alongside work
- Comprehensive training courses and individual opportunities for personal and professional further development
- Extensive company health management
- Ideal conditions for balancing work and private life, as well as a family-friendly corporate policy
- Full-time position with the option of slightly reduced working hours and 30 days of annual leave
- Targeted services for international employees, e.g. through our International Advisory Service

We offer you an exciting and varied role in an international and interdisciplinary working environment. The position is initially for a fixed term of 2 years, with possible long-term prospects. Salary and social benefits in conformity with the provisions of the Collective Agreement for the Civil Service (TVöD).

Forschungszentrum Jülich promotes equal opportunities and diversity in its employment relations.
We also welcome applications from disabled persons.