



JUWELS & JURECA Tuning for the platform

Usage of ParaStation MPI

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Patrick Küven ParTec AG



- ParaStation MPI
- Compiling your program
- Running your program
- Tuning parameters
- Resources



Partec ParaStation History

- 1995: University project (→ University of Karlsruhe)
- 2005: Open source (→ ParaStation Consortium)
- since 2004: Cooperation with JSC
 - various precursor clusters
 - DEEP-System (MSA Prototype)
 - JuRoPA3 (J3)
 - JUAMS
 - JURECA (Cluster/Booster)
 - JUWELS (Cluster/Booster)
 - JURECA DC









ParaStation MPI

- Based on MPICH (3.4.3)
 - supports all MPICH tools (tracing, debugging, ...)
- Proven to scale up to 3,300 nodes and 136,800 procs per job running ParaStation MPI
 - JUWELS: No. 77 (Top500 Nov 2021)
 - JURECA DC: No. 52 (Top500 Nov 2021)
 - JUWELS Booster: No. 8 (Top500 Nov 2021)
- Supports a wide range of interconnects, even in parallel
 - InfiniBand on JURECA Cluster and JUWELS
 - Omni-Path on JURECA Booster
 - Extoll on DEEP projects research systems
- Tight integration with Cluster Management (e.g. healthcheck)
- MPI libraries for several compilers
 - especially for GCC and Intel

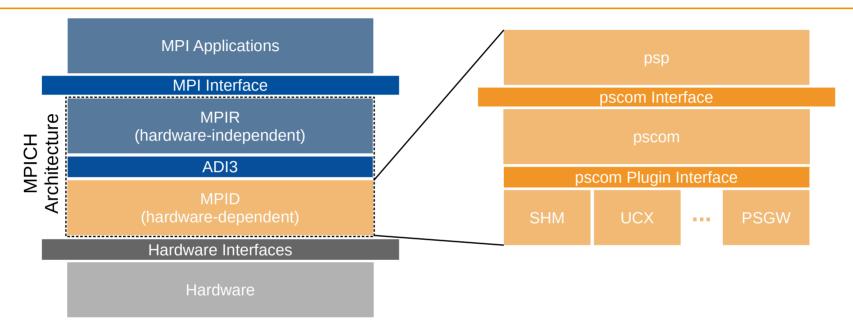


arTec ParaStation MPI: Modularity

- 2 or more different modules with different hardware
- a job can execute dynamically on all modules
- you can pick the best out of all the worlds in a single job
- e.g. JURECA:
 - DC: AMD EPYC + NVidia A100 + Infiniband
 - Booster: Intel KNL + Omni-Path
- How do these modules communicate with each other?



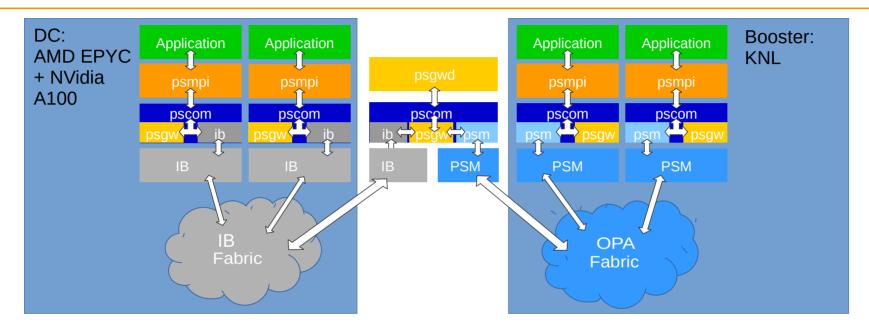
ParaStation MPI: pscom



- Low-level communication layer supporting various transports and protocols
- Applications may use multiple transports at the same time



ParaStation MPI: pscom



- For the JURECA DC-Booster System, the ParaStation MPI Gateway Protocol bridges between Mellanox IB and Intel Omni-Path
- In general, the ParaStation MPI Gateway Protocol can connect any two low-level networks supported by pscom
- Implemented using the psgw plugin to pscom, working together with instances of the psgwd



ParaStation MPI: Modular Jobs

- Two processes communicate through a gateway, if they are not directly connected by a high-speed network (e.g. IB or OPA)
- Static routing to choose a common gateway
- High-speed connections between processes and gateway daemons
- Virtual connection between both processes through the gateway, transparent for the application
- Virtual connections are multiplexed through gateway connections
- Further information: apps.fz-juelich.de/jsc/hps/jureca/modular-jobs.html



ParaStation MPI: CUDA awareness

- CUDA awareness supported by the following MPI APIs
 - Point-to-point (e.g., MPI Send, MPI Recv, ...)
 - Collectives (e.g., MPI Allgather, MPI Reduce, ...)
 - One-sided (e.g., MPI Put, MPI Get, ...)
 - Atomics (e.g., MPI_Fetch_and_op, MPI_Accumulate, ...)
- CUDA awareness for all transports via staging
- CUDA optimization: UCX
- Ability to query CUDA awareness at compile- and runtime



arTec ParaStation MPI: CUDA awareness

- activate CUDA-awareness by meta modules
 - default configurations
- query CUDA-awareness:



Compiling on JUWELS

- Currently MPI-3.3 version (5.4.11-1) available
- single thread tasks
 - module load Intel ParaStationMPI
 - module load GCC ParaStationMPI
- multi-thread tasks (mt)
 - module load Intel ParaStationMPI/5.4.11-1-mt
 - no multi-thread GCC version available
- ChangeLog available with
 - less \$(dirname \$(which mpicc))/../ChangeLog
- Gnu and Intel compilers available
- module spider for getting current versions
- see also the previous talk JUWELS Introduction



- Wrappers
 - mpicc (C)
 - mpicxx (C++)
 - mpif90 (Fortran 90)
 - mpif77 (Fortran 77)
- When using OpenMP and the need to use the "mt" version, add
 - fopenmp (GNU)
 - -qopenmp (Intel)



arTec Did I use the wrapper correctly?

- Libraries are linked at runtime according to LD_LIBRARY_PATH
- 1dd shows the libraries attached to your binary
- Look for ParaStation libraries

```
ldd hello mpi:
libmpi.so.12 => /p/software/juwels/stages/2020/
software/psmpi/5.4.7-1-iccifort-2020.2.254-GCC-9.3.0/
lib/libmpi.so.12 (0x000015471ea43000)
VS.
libmpi.so.12 => /p/software/juwels/stages/2020/
software/psmpi/
5.4.7-1-iccifort-2020.2.254-GCC-9.3.0-mt/lib/
libmpi.so.12 (0x000014f110e58000)
```



arTec JUWELS: start via srun

- Use srun to start MPI processes
- srun -N <nodes> -n <tasks> spawns task
 - directly (-A <account>)
 - via salloc
 - from batch script via sbatch
- Exports full environment
- Stop interactive run with (consecutive) ^C
 - passed to all tasks
- No manual clean-up needed
- You can log into nodes which have an allocation/running job step
 - 1) squeue -u <user>
 - 2) sgoto <jobid> <nodenumber>
 - e.g. sgoto 2691804 0

'arTec hello mpi.c

```
/* C Example */
#include <stdio.h>
#include <mpi.h>
int main (int argc, char **argv) {
  int numprocs, rank, namelen;
  char processor name[MPI MAX PROCESSOR NAME];
  MPI_Init (&argc, &argv);
  MPI_Comm_rank (MPI_COMM_WORLD, &rank);
  MPI_Comm_size (MPI_COMM_WORLD, &numprocs);
  MPI_Get_processor_name (processor_name, &namelen);
  printf ("Hello world from process %d of %d on %s\n",
          rank, numprocs, processor_name);
  MPI_Finalize ();
  return 0;
```

Running on JUWELS (Intel chain)

- module load Intel
- module load ParaStationMPI
- mpicc -03 -o hello mpi hello mpi.c
- Interactive:
- salloc -N 2 -A partec # get an allocation
- srun -n 2 ./hello mpi

Hello world from process 0 of 2 on jwc08n188.juwels Hello world from process 1 of 2 on jwc08n194.juwels

- Batch:
- sbatch ./hello_mpi.sh
- Increase verbosity:
 - PSP_DEBUG=[1,2,3,...] srun -n 2 ./hello_mpi



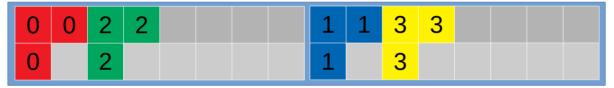
arTec Process Placement

- ParaStation process pinning:
 - Avoid task switching
 - Make better use of CPU cache and memory bandwidth
- JUWELS is pinning by default:
 - So --cpu-bind=threads may be omitted
- Manipulate pinning:
 - e.g. for "large memory / few task" applications
- Manipulate via
 - --cpu-bind=threads|sockets|cores|
 mask_cpu:<mask1>,<mask2>,...
 - CPU masks are always interpreted as hexadecimal values
 - --distribution=*|block|cyclic|arbitrary|plane=<options>
 [:*|block|cyclic|fcyclic[:*|block|cyclic|fcyclic]][,Pack|NoPack]
- Further information: https://apps.fz-juelich.de/jsc/hps/juwels/affinity.html



ParTec Process Placement

- Example:
- --ntasks-per-node=4
- --cpus-per-task=3
- --cpu-bind=threads



■ --cpu-bind=mask cpu:0x7,0x700,0xE0,0xE000





Process Placement

Best practice depends not only on topology, but also on characteristics of application:

- Putting threads far apart is
 - improving the aggregated memory bandwidth available to your application
 - improving the combined cache size available to your application
 - decreasing the performance of synchronization constructs
- Putting threads close together is
 - improving the performance of synchronization constructs
 - decreasing the available memory bandwidth and cache size



CarTec Hybrid MPI/OpenMP

```
Example:
#include <stdio.h>
#include <mpi.h>
                                                           2 Nodes, 2x2 Procs.
#include <omp.h>
                                                           2x2x24 Threads
int main(int argc, char *argv[]) {
  int numprocs, rank, namelen;
                                                             Node x
                                                                           Node y
  char processor_name[MPI_MAX_PROCESSOR_NAME];
  int iam = 0, np = 1;
 MPI_Init(&argc, &argv);
 MPI_Comm_size(MPI_COMM_WORLD, &numprocs);
 MPI_Comm_rank (MPI_COMM_WORLD, &rank);
 MPI_Get_processor_name (processor_name, &namelen);
#pragma omp parallel default(shared) private(iam, np)
   np = omp_get_num_threads();
   iam = omp_get_thread_num();
   printf("Hello from thread %02d out of %d from process %d out of %d on %s\n",
          iam, np, rank, numprocs, processor_name);
 MPI_Finalize();
```

arTec On JUWELS

- module load Intel ParaStationMPI/5.4.11-1-mt
- mpicc -03 -qopenmp -o hello hybrid hello hybrid.c
- salloc -N 2 -A partec --cpus-per-task=24
- export OMP NUM THREADS=\${SLURM CPUS PER TASK}
- srun -n 4 ./hello hybrid | sort

```
Hello from thread 00 out of 24 from process 0 out of 4 on jwc01n238.juwels Hello from thread 00 out of 24 from process 1 out of 4 on jwc01n238.juwels Hello from thread 00 out of 24 from process 2 out of 4 on jwc01n247.juwels Hello from thread 00 out of 24 from process 3 out of 4 on jwc01n247.juwels Hello from thread 01 out of 24 from process 0 out of 4 on jwc01n238.juwels Hello from thread 01 out of 24 from process 1 out of 4 on jwc01n238.juwels Hello from thread 01 out of 24 from process 2 out of 4 on jwc01n247.juwels Hello from thread 01 out of 24 from process 3 out of 4 on jwc01n247.juwels Hello from thread 23 out of 24 from process 0 out of 4 on jwc01n238.juwels Hello from thread 23 out of 24 from process 1 out of 4 on jwc01n238.juwels Hello from thread 23 out of 24 from process 1 out of 4 on jwc01n238.juwels Hello from thread 23 out of 24 from process 2 out of 4 on jwc01n238.juwels Hello from thread 23 out of 24 from process 2 out of 4 on jwc01n247.juwels Hello from thread 23 out of 24 from process 3 out of 4 on jwc01n247.juwels
```



Pinning

- JUWELS:
 - 2 Sockets, 24 Cores per Socket
 - 2 HW-Threads per Core
 - → 96 HW-Threads possible
- Normally (SMT):
 - HW-Threads 0-23, 48-71 → CPU0
 - HW-Threads 24-47, 72-95 → CPU1

"Package"

Node										
Socket 0					Socket 1					
Core 0	Core 1	•••	Core 22	Core 23	Core 24	Core 25		Core 46	Core 47	
HWT 0	HWT 1		HWT 22	HWT 23	HWT 24	HWT 25		HWT 46	HWT 47	
HWT 48	HWT 49		HWT 70	HWT 71	HWT 72	HWT 73		HWT 94	HWT 95	



Pinning

- JURECA DC:
 - 2 Sockets, 64 Cores per Socket
 - 2 HW-Threads per Core
 - → 256 HW-Threads possible
- Normally (SMT):
 - HW-Threads 0-63, 128-191 → CPU0

"Package"

HW-Threads 64-127, 192-255 → CPU1

Node									
Socket 0					Socket 1				
Core 0	Core 1		Core 62	Core 63	Core 64	Core 65		Core 126	Core 127
HWT 0	HWT 1		HWT 62	HWT 63	HWT 64	HWT 65		HWT 126	HWT 127
HWT 128	HWT 129		HWT 190	HWT 191	HWT 192	HWT 193		HWT 254	HWT 255

Pinning

- No thread pinning by default on JURECA and JUWELS
- Allow the Intel OpenMP library thread placing
 - export KMP_AFFINITY=[verbose, modifier,]...
 compact: place threads as close as possible
 scatter: as evenly as possible
- Full environment is exported via srun on JURECA and JUWELS
- For GCC: set GOMP_CPU_AFFINITY (see manual)



Large Job Considerations

- Every MPI process talks to all others:
 - (N-1) x 0.55 MB communication buffer space per process!
- Example 1 on JUWELS:
 - job size 256 × 96 = 24,576 processes
 - 24,575 × 0.55 MB \rightarrow ~ 13,516 MB / process
 - \times 96 process / node \rightarrow \sim 1,267 GB communication buffer space
 - But there is only 96 GB of main memory per node
- Example 2 on JURECA DC:
 - job size 256 × 256 = 65,536 processes
 - $65,535 \times 0.55 \text{ MB} \rightarrow \sim 36,044 \text{ MB / process}$
 - × 256 process / node → ~ 9,011 GB mpi buffer space
 - But there is only 512 GB of main memory per node
- Example 3 on JURECA Booster:
 - ~ 10,173 GB mpi buffer space ↔ 96 GB of main memory per node

On Demand / Buffer Size

Three possible solutions:

- 1. Try using alternative meta modules
- 2. Create buffers on demand only:
 - export PSP ONDEMAND=1
 - Activated by default!
- 3. Reduce the buffer queue length:
 - (Default queue length is 16)
 - export PSP OPENIB SENDQ SIZE=3
 - export PSP_OPENIB_RECVQ_SIZE=3
 - Do not go below 3, deadlocks might occur!
 - Trade-off: Performance penalty
 - (sending many small messages)





arTec On Demand / Queue Size Guidelines

- On-Demand works best with nearest neighbor communications
 - (Halo) Exchange
 - Scatter/Gather
 - All-reduce
 - **-** ...
- But for All-to-All communication:
 - queue size modification only viable option...
- Example

```
rank 0: for (;;) MPI_Send ()
rank 1: for (;;) MPI_Recv ()

- PSP_OPENIB_SENDQ/RECVQ_SIZE=4: 1.8 seconds
- PSP_OPENIB_SENDQ/RECVQ_SIZE=16: 0.6 seconds
- PSP_OPENIB_SENDQ/RECVQ_SIZE=64: 0.5 seconds
```



Resources

- www.par-tec.com
- www.fz-juelich.de/ias/jsc/EN/Expertise/Supercomputers/supercomputers_ node.html
- /opt/parastation/doc/pdf
- by mail: sc@fz-juelich.de
- by mail: support@par-tec.com
- Download ParaStation MPI at github:
 - https://github.com/ParaStation/psmgmt
 - https://github.com/ParaStation/pscom
 - https://github.com/ParaStation/psmpi



Partec Summary Modular Supercomputing

- You now should be able to
 - compile
 - run your application
 - tune some runtime parameters
 - diagnose and fix specific errors
 - know where to turn to in case of problems



Partec Thank you for your attention!

Questions?