

WALBERLA, an ultra-scalable multi-physics simulation framework for piecewise regular grids

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Chair for System Simulation

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TECHNISCHE FAKULTÄT

- Introduction
 - the *waLBerla* Simulation Framework
- Lattice Boltzmann method
 - domain decomposition & parallelization
 - uniform grids: performance / benchmarks
 - grid refinement
 - refined grids: performance / benchmarks
- Phase-field Methods
 - application & performance
- Outlook / Conclusion

- originally designed for **CFD**
- using the **lattice Boltzmann method** (LBM)
- large-scale particulate flows: coupling with in-house rigid body physics engine *pe*
- Free surface module (VOF)
- designed as an **HPC** software framework:
 - scales from laptops to current petascale supercomputers
 - largest simulation: 1,835,008 processes (IBM Blue Gene/Q @ Jülich)
 - **hybrid parallelization**: MPI + OpenMP
 - **hand-crafted** compute kernels for optimal performance

- written in **C++(11)**, optional **Python** interface
- support for different platforms (Linux, Windows, MacOS) and compilers (GCC, Intel XE, Visual Studio, llvm/clang, IBM XL)
- Large number of algorithms & supported platforms lead to high maintenance cost
- CI-workflow with automated testing on available target platforms



LBM on Uniform Grids

- Domain Decomposition & Parallelization
- Performance / Benchmarks

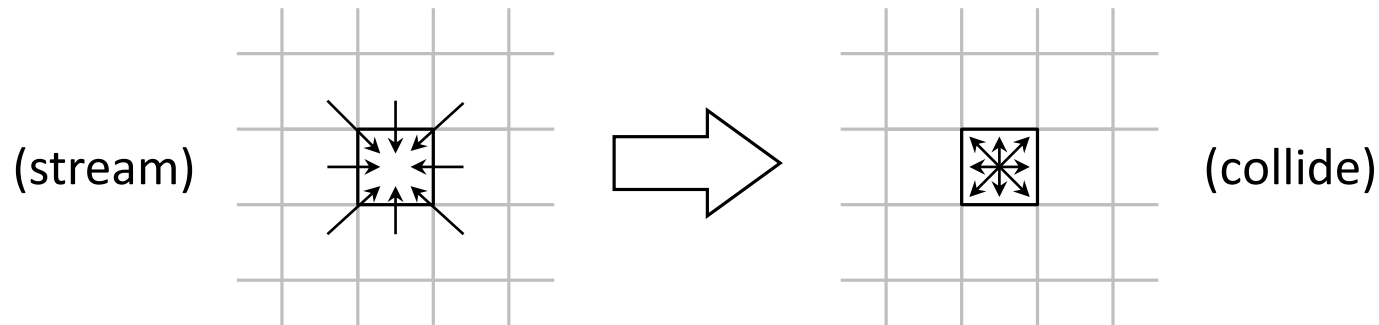
C. Godenschwager, F. Schornbaum, M. Bauer, H. Köstler, and U. Rüde, *A Framework for Hybrid Parallel Flow Simulations with a Trillion Cells in Complex Geometries*, SC13, Denver



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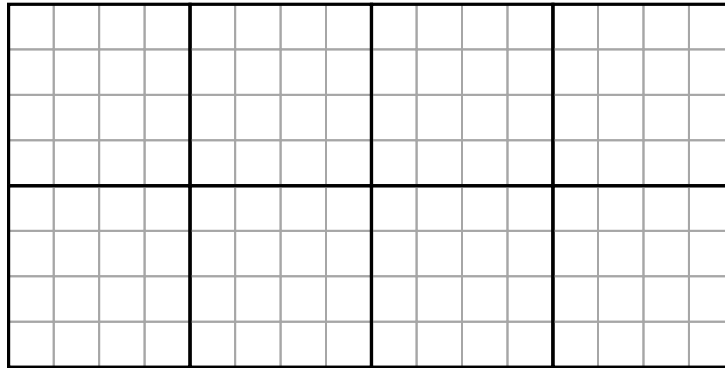
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- regular grid with multiple particle distribution functions (= scalar values) per cell [D2Q9, D3Q19, D3Q27, ...]
- explicit method → time stepping
- two steps: stream (neighbors) & collide (cell-local)

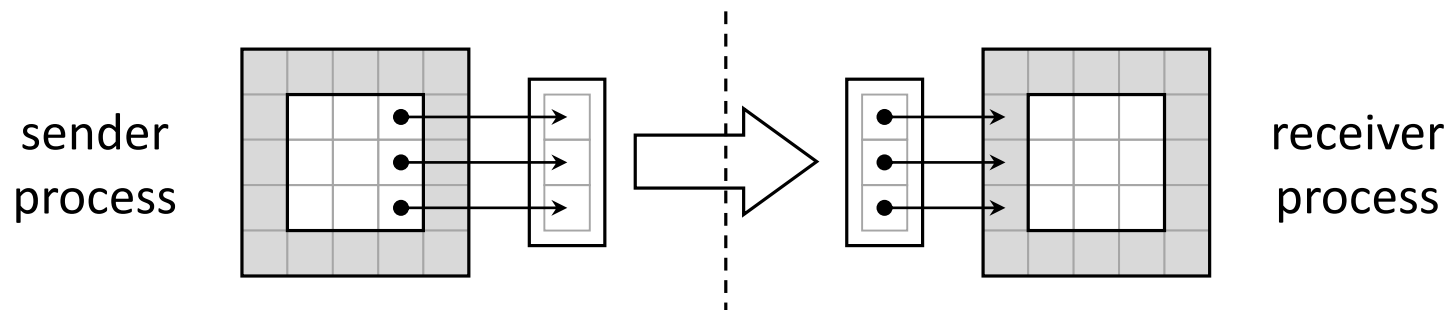


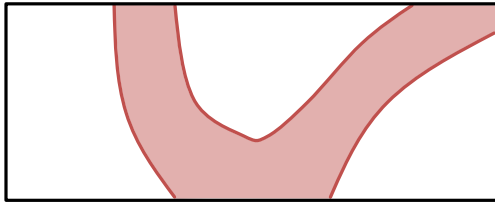
- For the collision, different operators exist: SRT, TRT, MRT.
- Macroscopic quantities (velocity, density, ...) can be calculated from the particle distribution functions.

- Domain Decomposition:
 - regular decomposition into blocks containing uniform grids

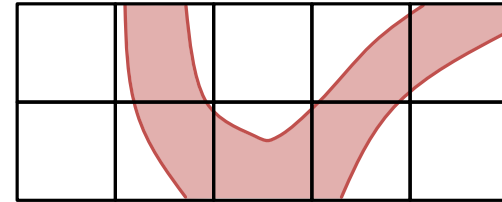


- Parallelization:
 - data exchange on borders between blocks via ghost layers





geometry given by surface mesh



domain decomposition into blocks



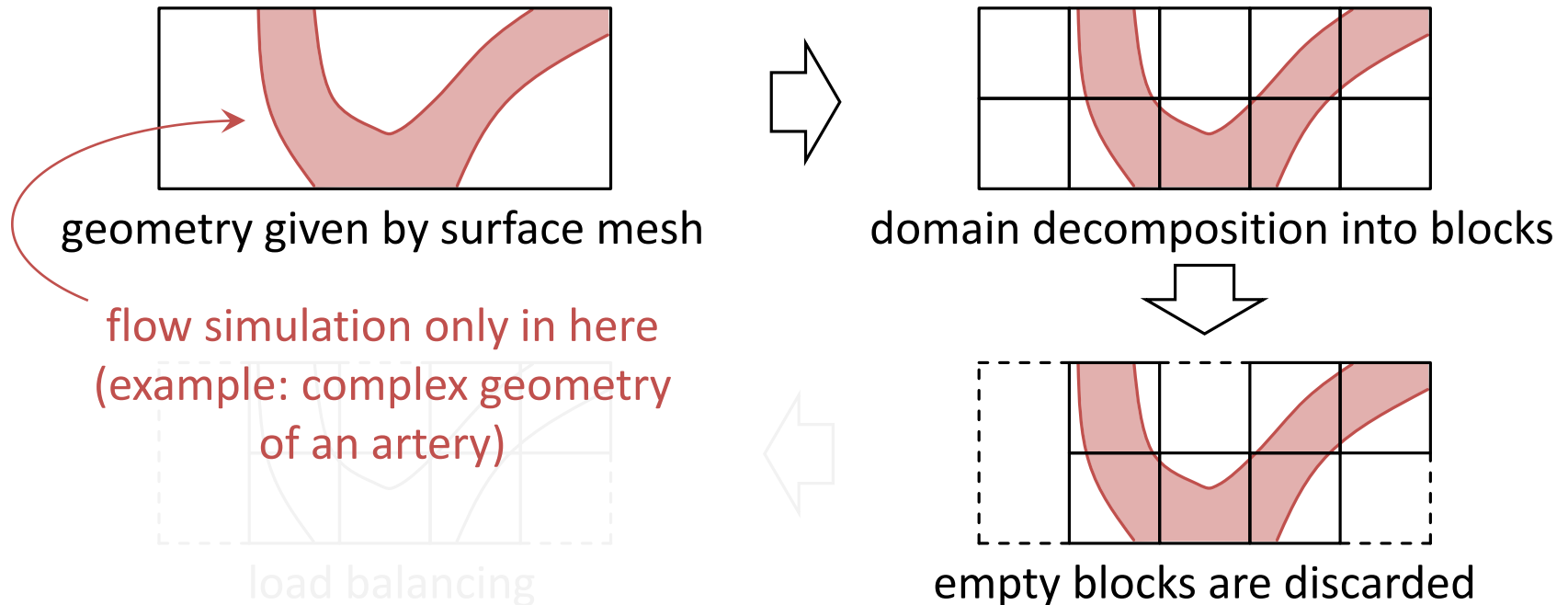
load balancing



empty blocks are discarded

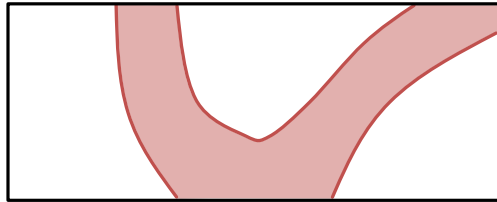
Load balancing can be based on either space-filling curves (Z-order/Morton order, Hilbert curve) using the underlying forest of octrees or graph partitioning (METIS, ...).

Whatever fits best the needs of the simulation.

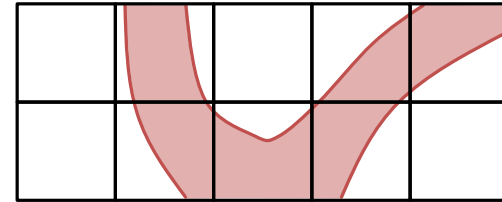


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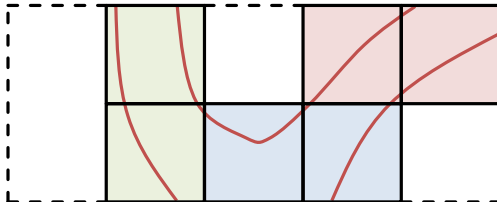
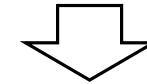
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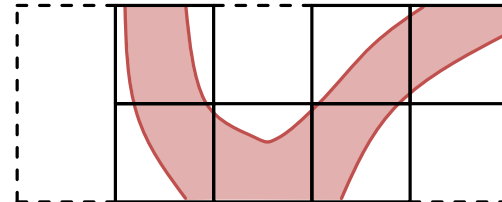
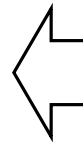
geometry given by surface mesh



domain decomposition into blocks



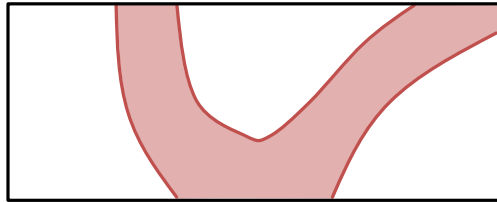
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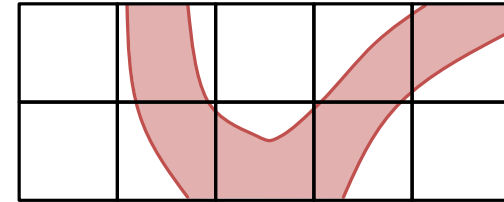
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Load balancing by space-filling curves
(Z-order/Morton order, Hilbert curve)
or graph partitioning (METIS, ...).

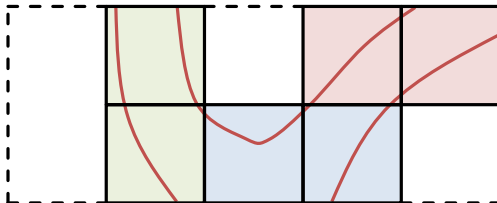
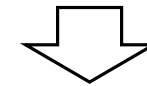
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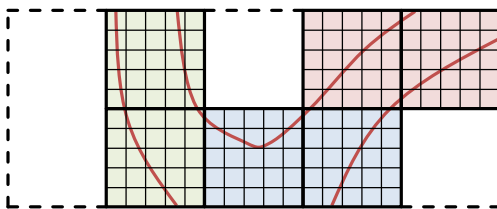
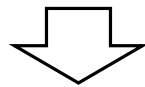
geometry given by surface mesh



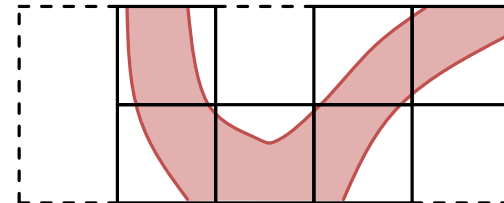
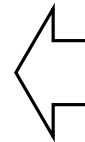
domain decomposition into blocks



load balancing

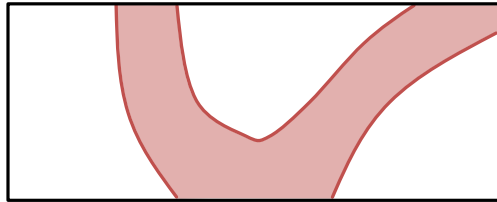


allocation of block data (→ grids)

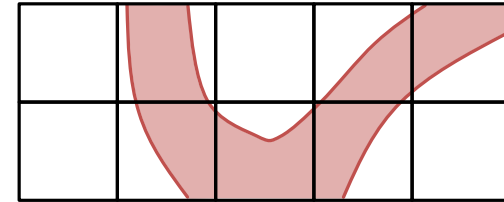


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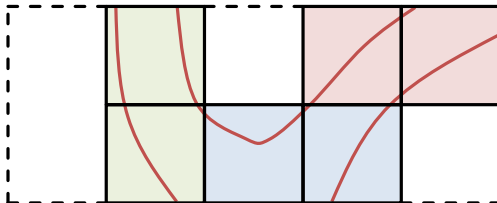
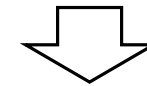
The domain decomposition and load balancing can be performed during the actual simulation ... OR ...



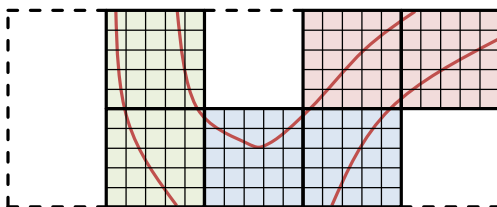
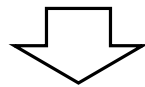
geometry given by surface mesh



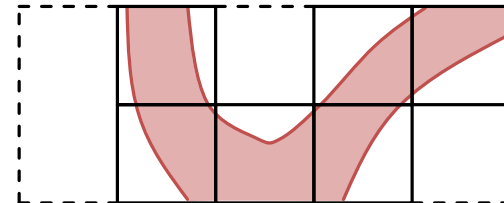
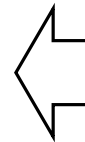
domain decomposition into blocks



load balancing

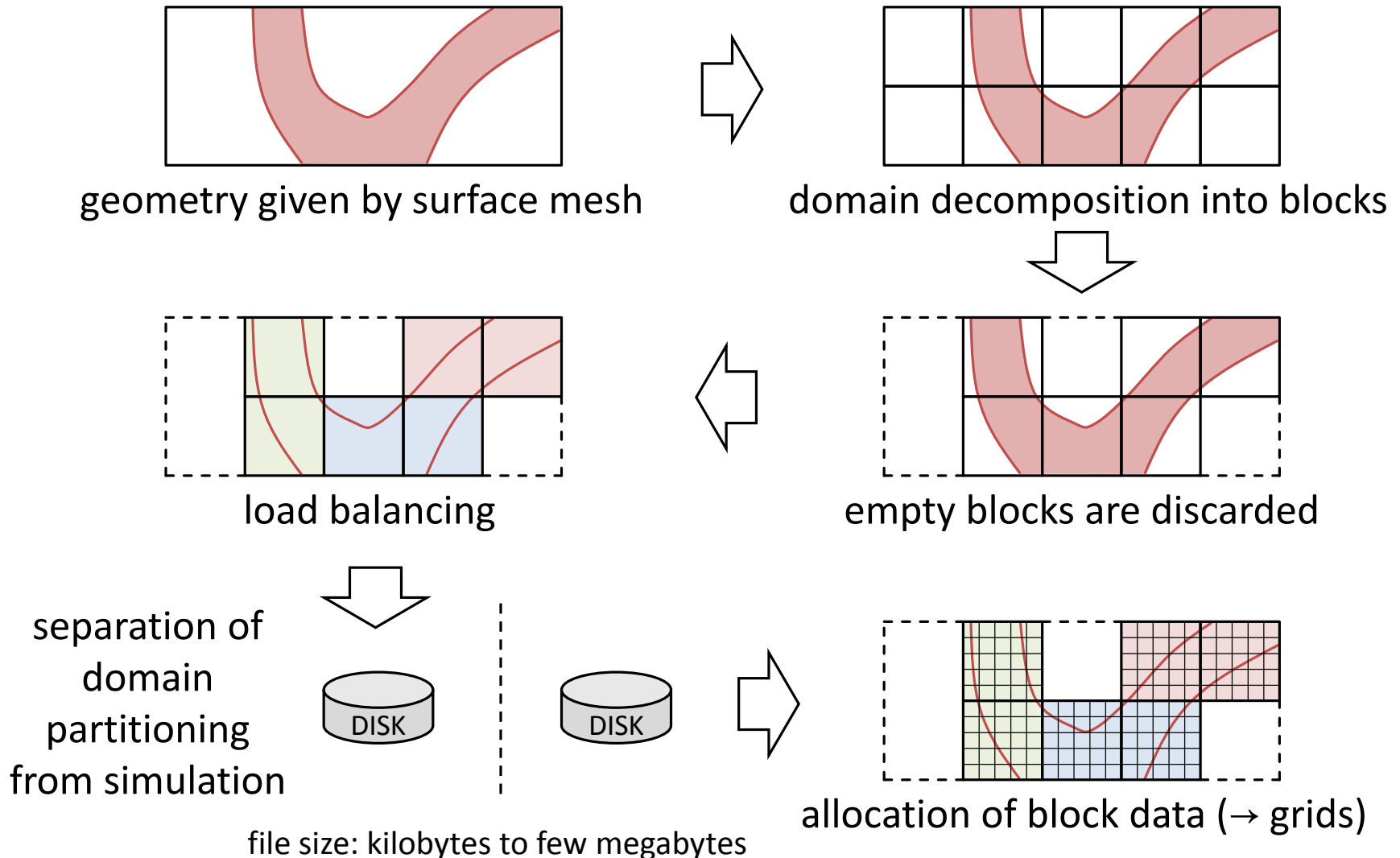


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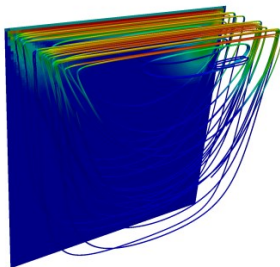


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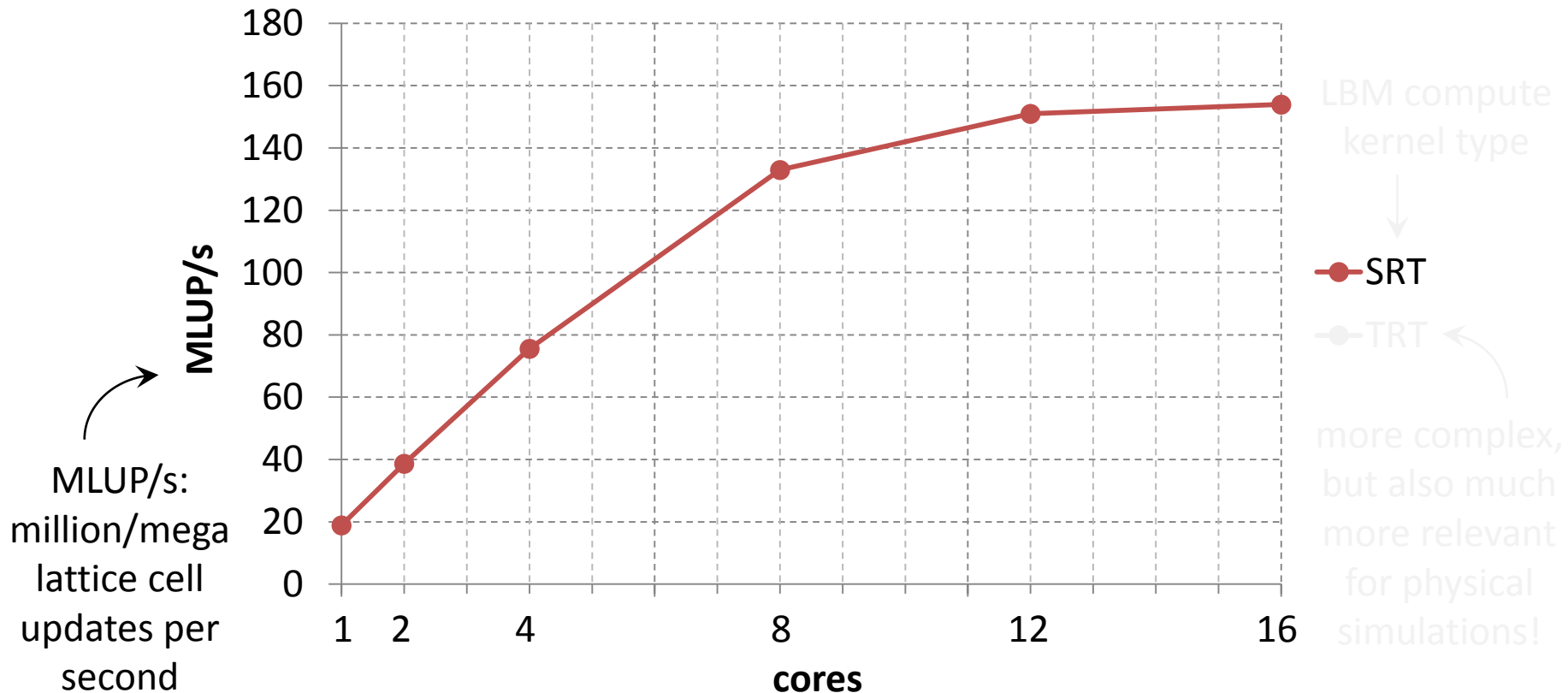
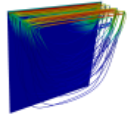


- Benchmark Environments:
 - JUQUEEN
 - Blue Gene/Q, 459K cores, 1 GB/core
 - compiler: IBM XL / IBM MPI
 - SuperMUC (Sandy Bridge)
 - Intel Xeon, 147K cores, 2 GB/core
 - compiler: Intel XE / IBM MPI
- Benchmark (LBM D3Q19):



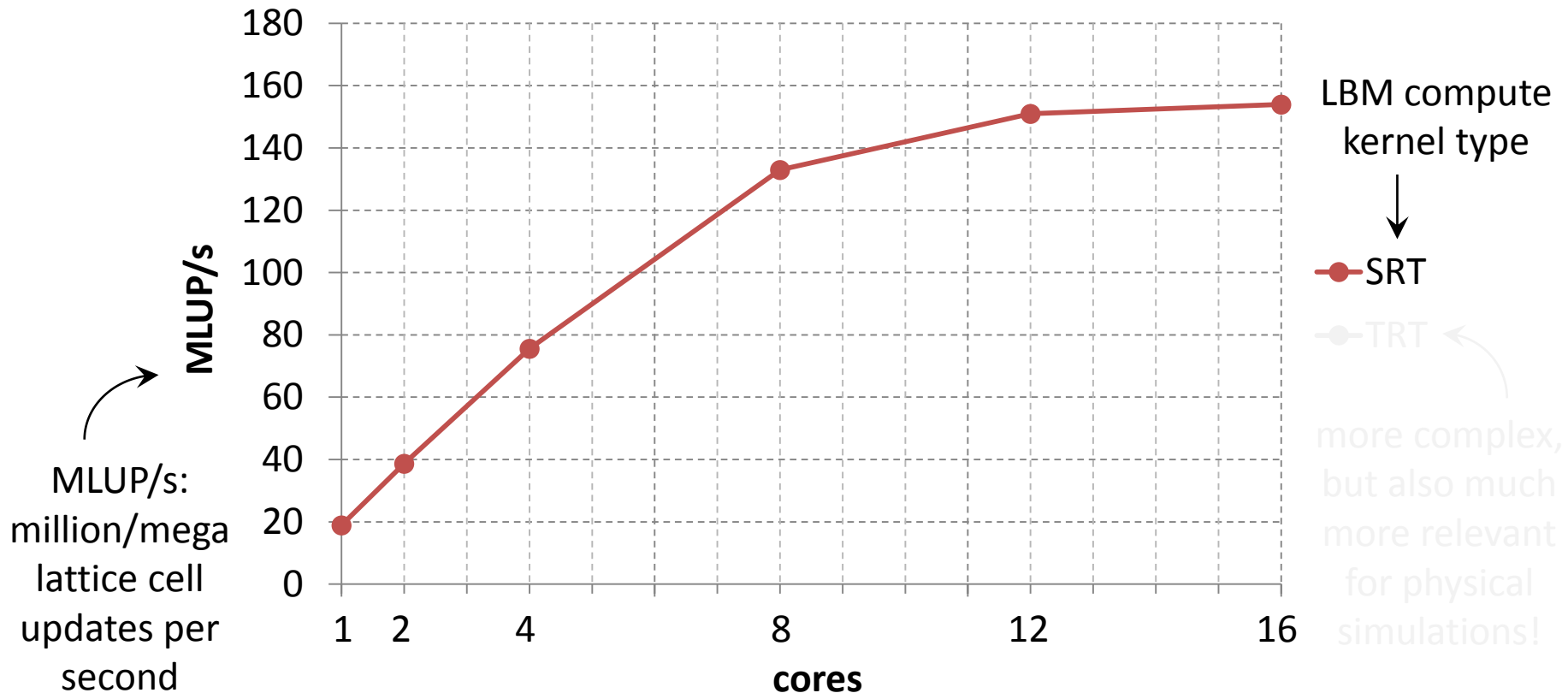
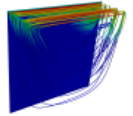
lid-driven cavity
weak scaling
(= const. number
of cells per core)

- SuperMUC – single node (3.34 million cells per core)



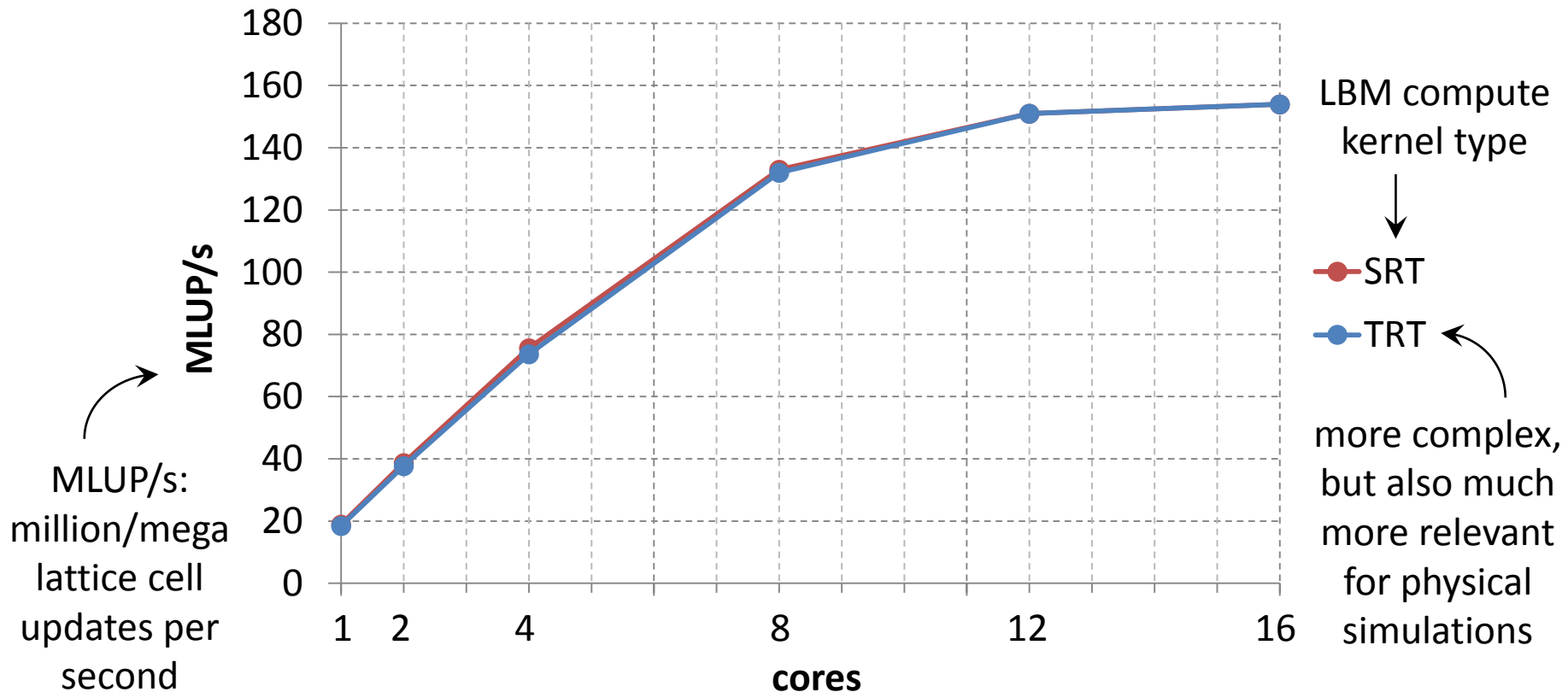
⇒ LBM: low FLOPs to bytes ratio → memory intensive

- SuperMUC – single node (3.34 million cells per core)



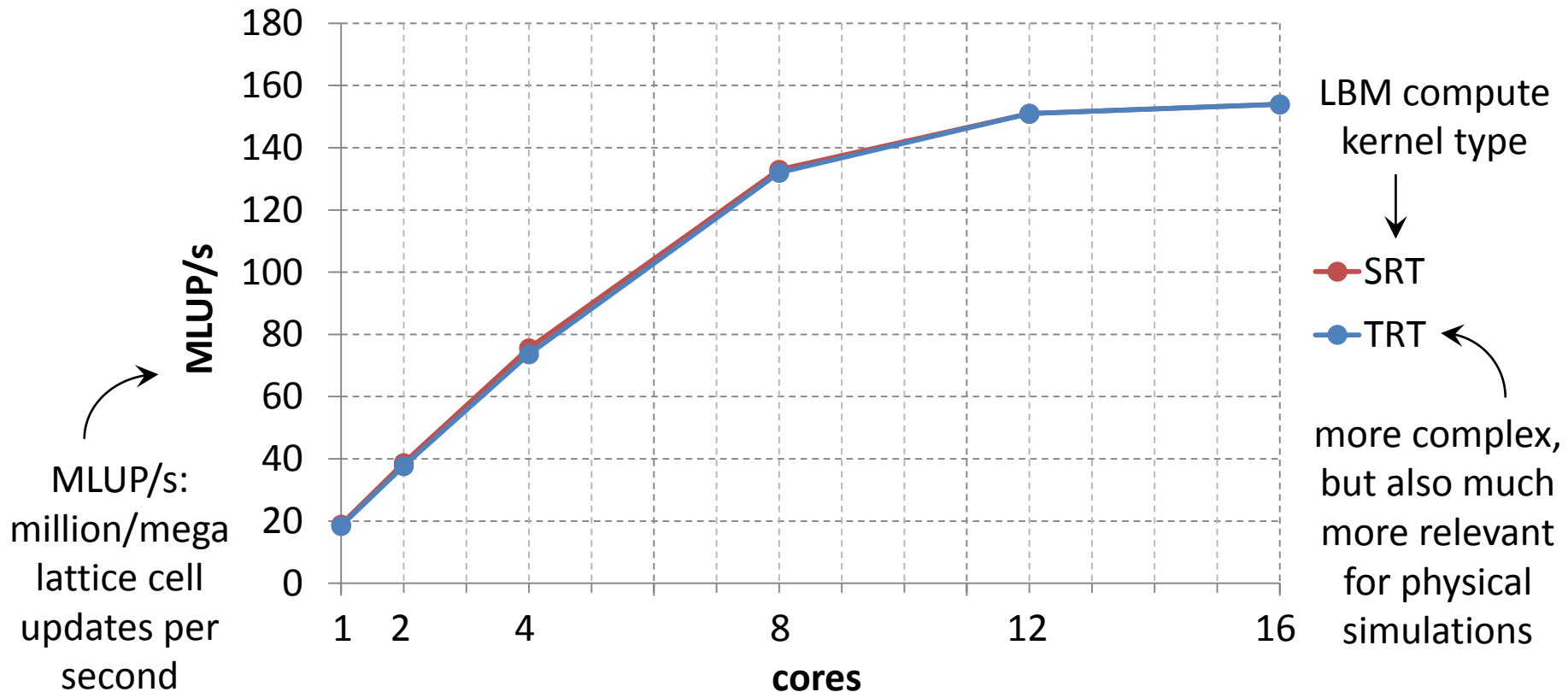
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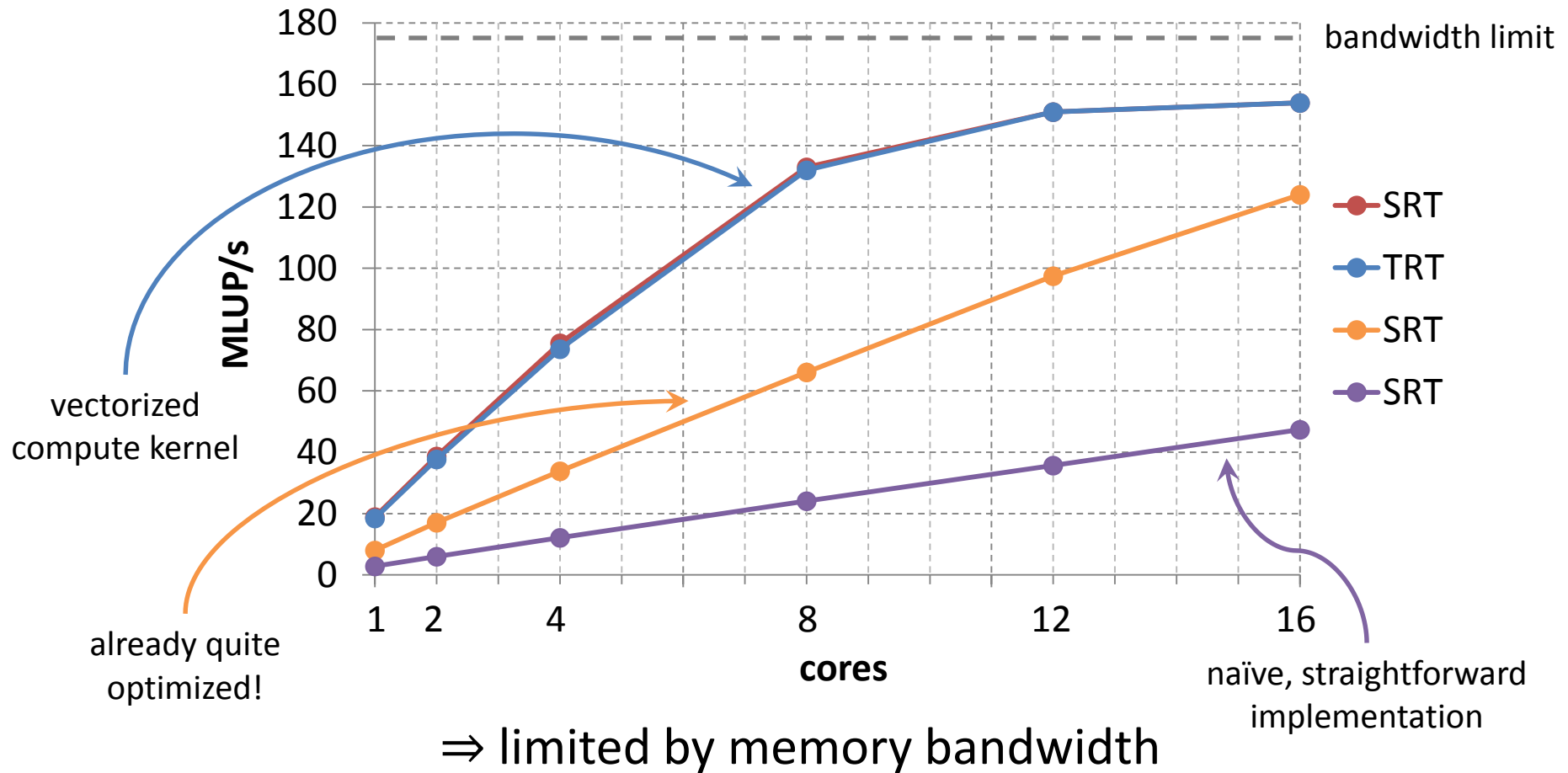
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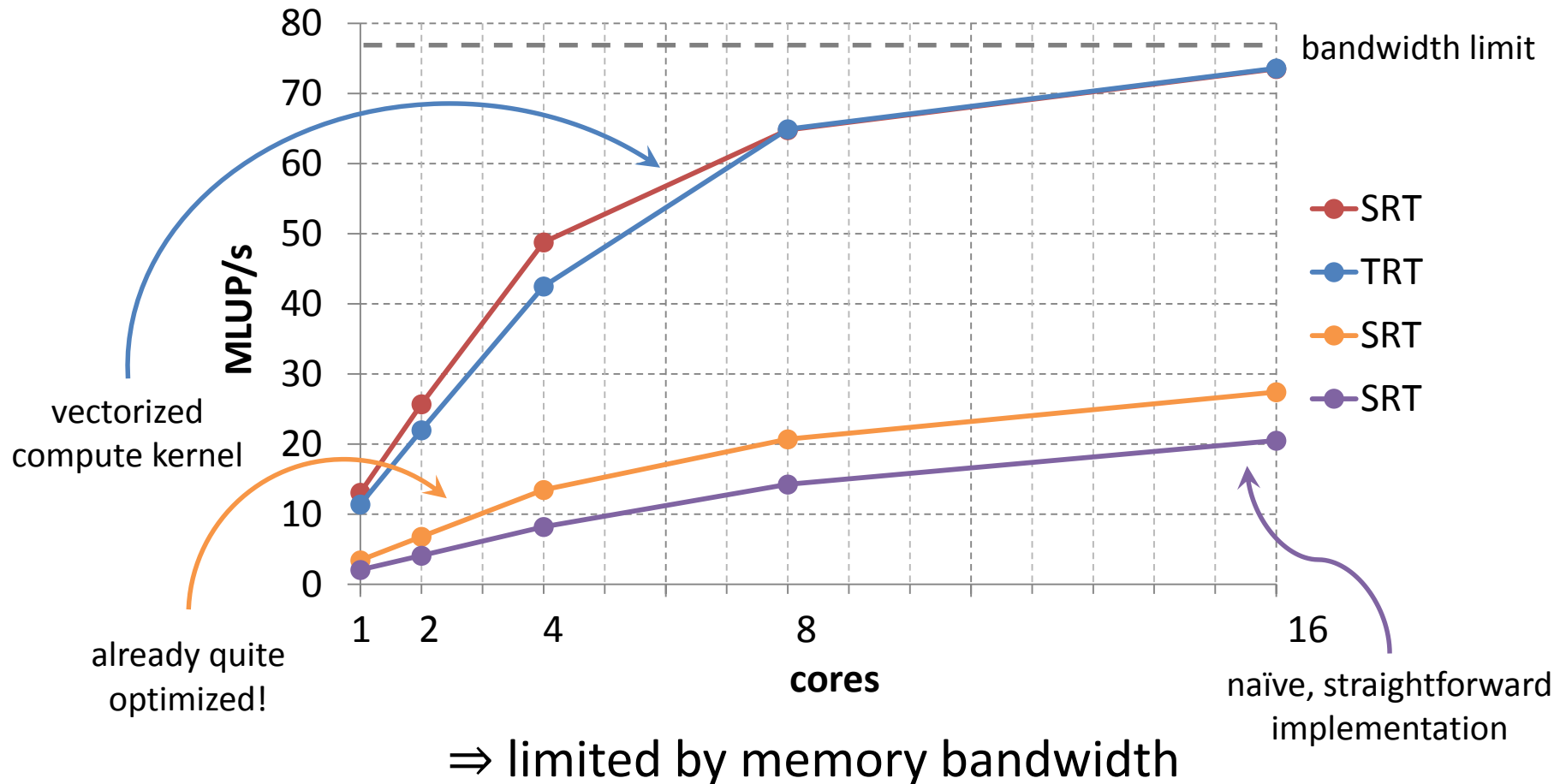


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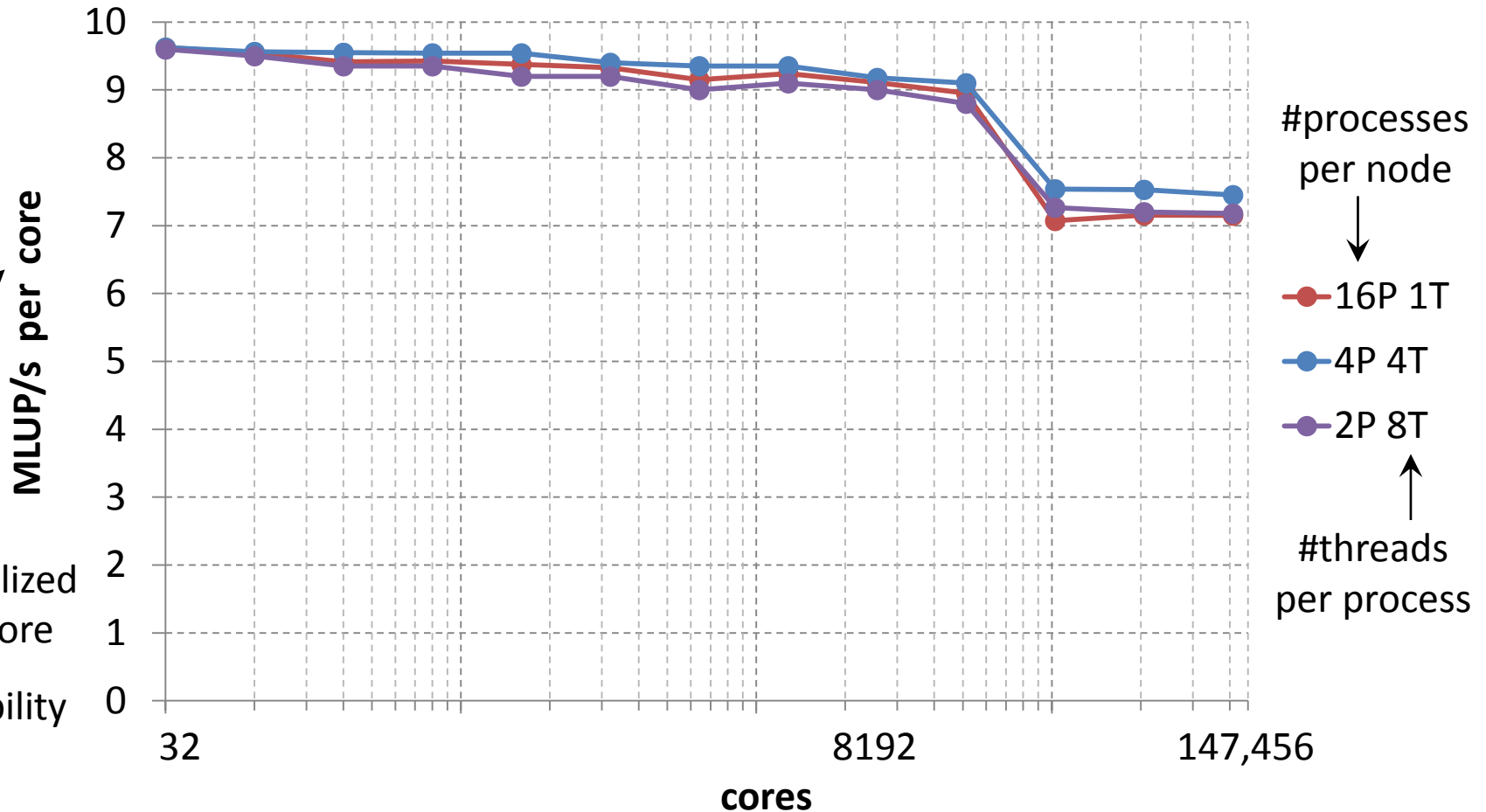
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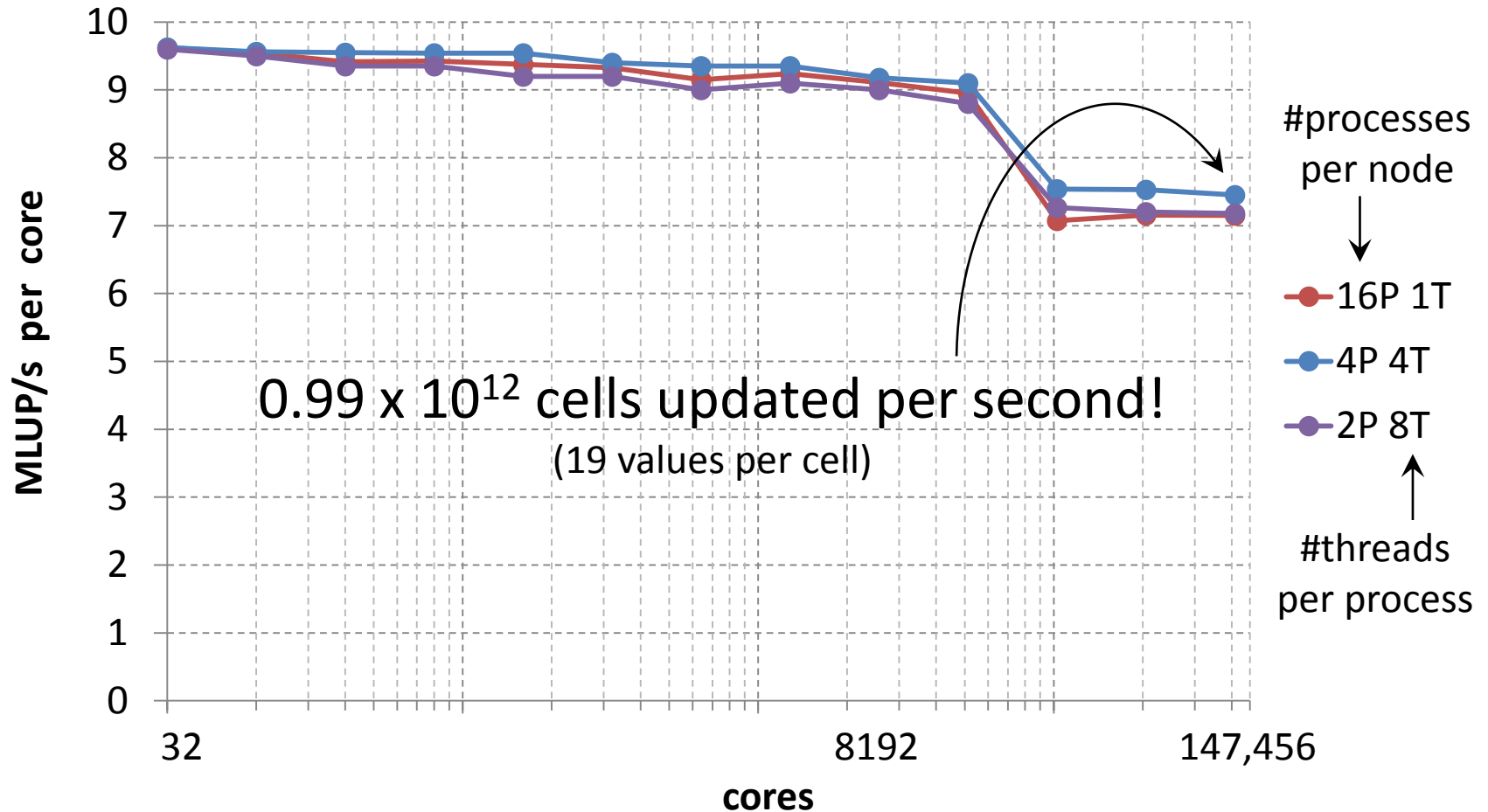
- JUQUEEN – single node (1.73 million cells per core)



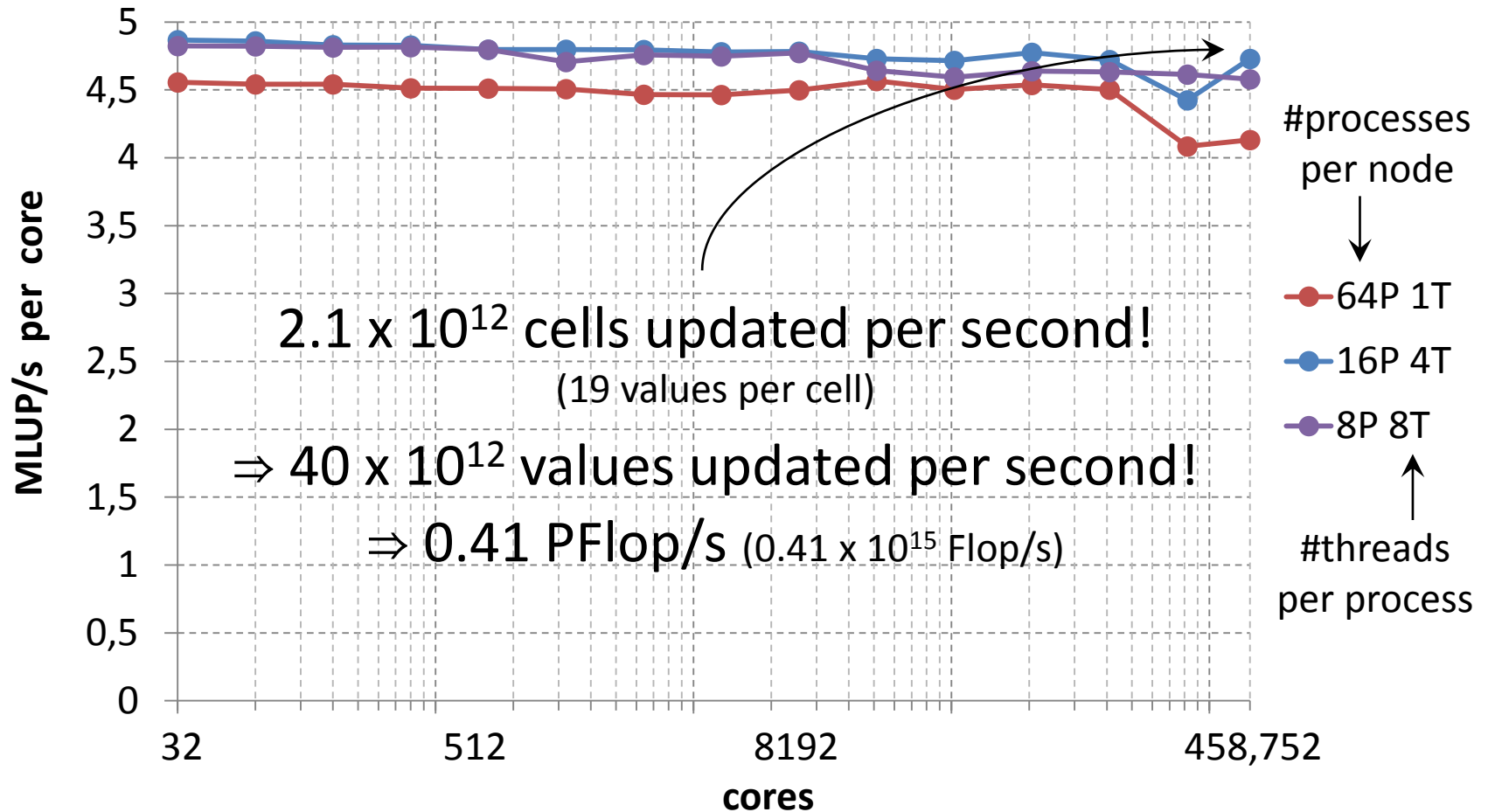
- SuperMUC – TRT kernel (3.34 million cells per core)



- SuperMUC – TRT kernel (3.34 million cells per core)



- JUQUEEN – TRT kernel (1.73 million cells per core)





Statically Refined Grids

- Lattice Boltzmann & Grid Refinement
- Domain Decomposition & Load Balancing
- Performance / Benchmarks

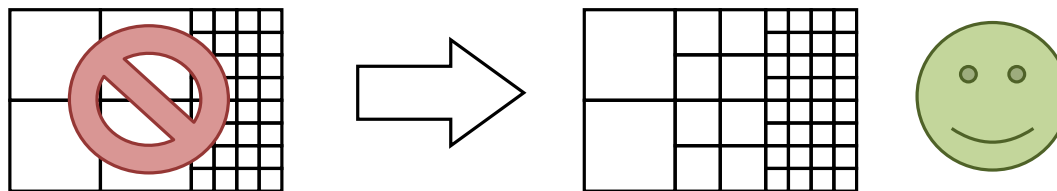
F. Schornbaum and U. Rde, 2015, *Massively Parallel Algorithms for the Lattice Boltzmann Method on Non-uniform Grids*, arXiv:1508.07982, submitted to SISC



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- Almost all grid refinement schemes for the lattice Boltzmann method rely on a **2:1 balance** between neighboring cells:



- memory requirement increases by a factor of 8 and the generated workload by a factor of 16
- twice as many time steps on the fine grid** as on the next coarser grid

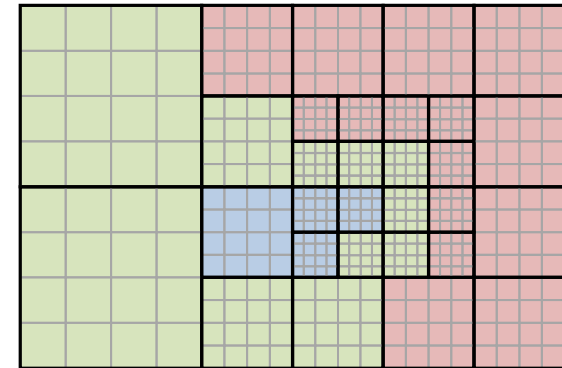
- Domain Decomposition:

- “blocks containing regular grids distributed among all processes”

⇒ **distributed forest of octrees**

(→ 2:1 balance)

- Each process only knows about its **own blocks** and their **neighbors**, but has no information about the **rest of the domain**.
 - ⇒ perfectly distributed data structure that **scales to huge numbers of processes** without any runtime overhead



- Property of this Distributed Data Structure:

- can be viewed as a graph: each block is connected to all of its neighboring blocks → enables all kinds of graph algorithms

- Comparison with Uniform LBM:
 - The **setup phase remains unchanged**:
The decoupling (via a file) of the domain decomposition & initial load balancing from the actual simulation is still possible.
 - All the refinement “magic” happens during communication (fine and coarse blocks share ghost layer regions → refinement requires **multiple ghost layers** per block).
 - **Time stepping becomes more complicated**
 - **All the compute kernels remain unchanged**

- Benchmark Environments:

- JUQUEEN

- Blue Gene/Q, 459K cores, 1 GB/core
 - compiler: IBM XL / IBM MPI

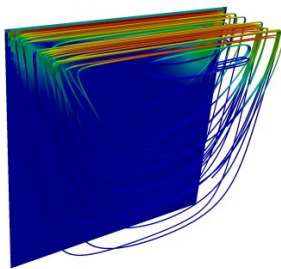


- SuperMUC

- Intel Xeon, 147K cores, 2 GB/core
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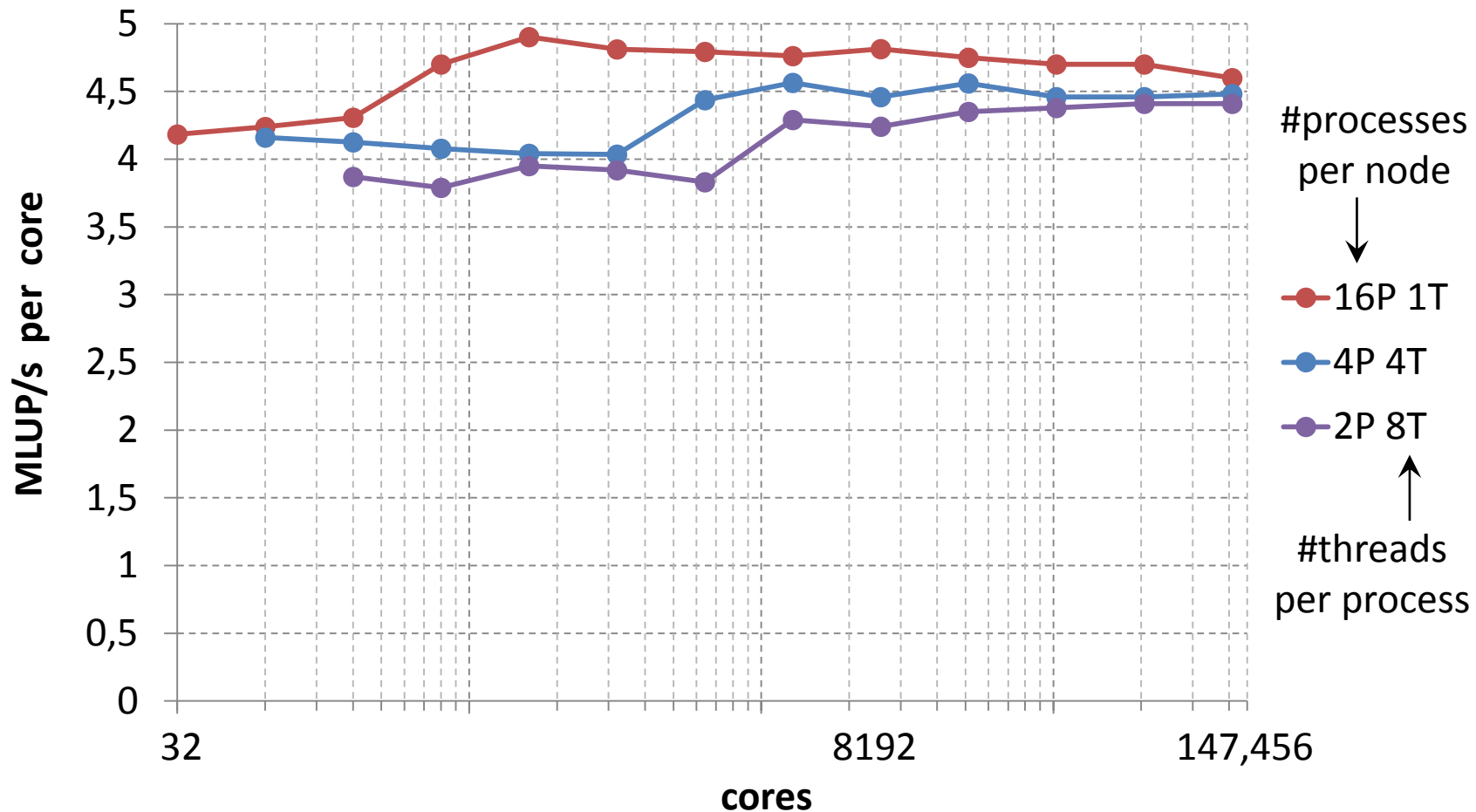
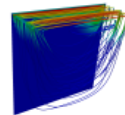
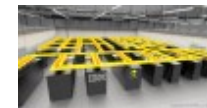
- Benchmark (LBM D3Q19 TRT):



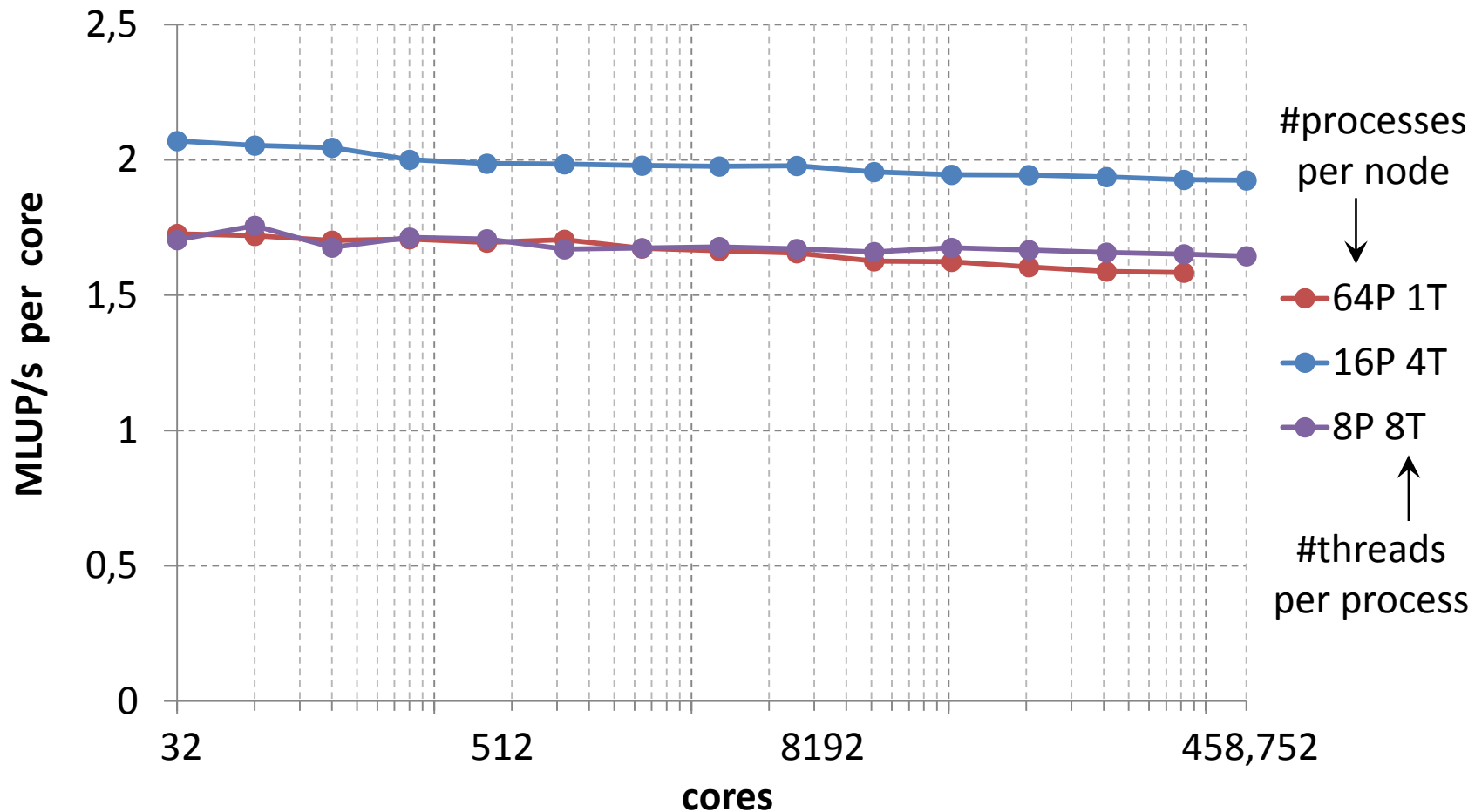
- lid-driven cavity
 - **weak scaling**
 - 6.69 blocks per process
 - **4 grid levels**

- finest level ...
 - ... covers 1.4% of space
 - ... generates 80% of workload
 - coarsest level ...
 - ... covers 78% of space
 - ... generates 1.1% of workload

- SuperMUC – TRT kernel (3.34 million cells per core)



- JUQUEEN – TRT kernel (1.81 million cells per core)



- LBM with refinement only achieves half the number of cell updates per second as regular LBM without refinement.
- Refinement Overhead:
 - much more complex communication patterns which additionally involve interpolation
 - more complex time stepping scheme
 - multiple blocks per process $\Leftrightarrow$ smaller blocks

BUT

LBM with grid refinement requires
much **less memory** and far **fewer cell updates**



Phase-field Method

- Application
- Performance

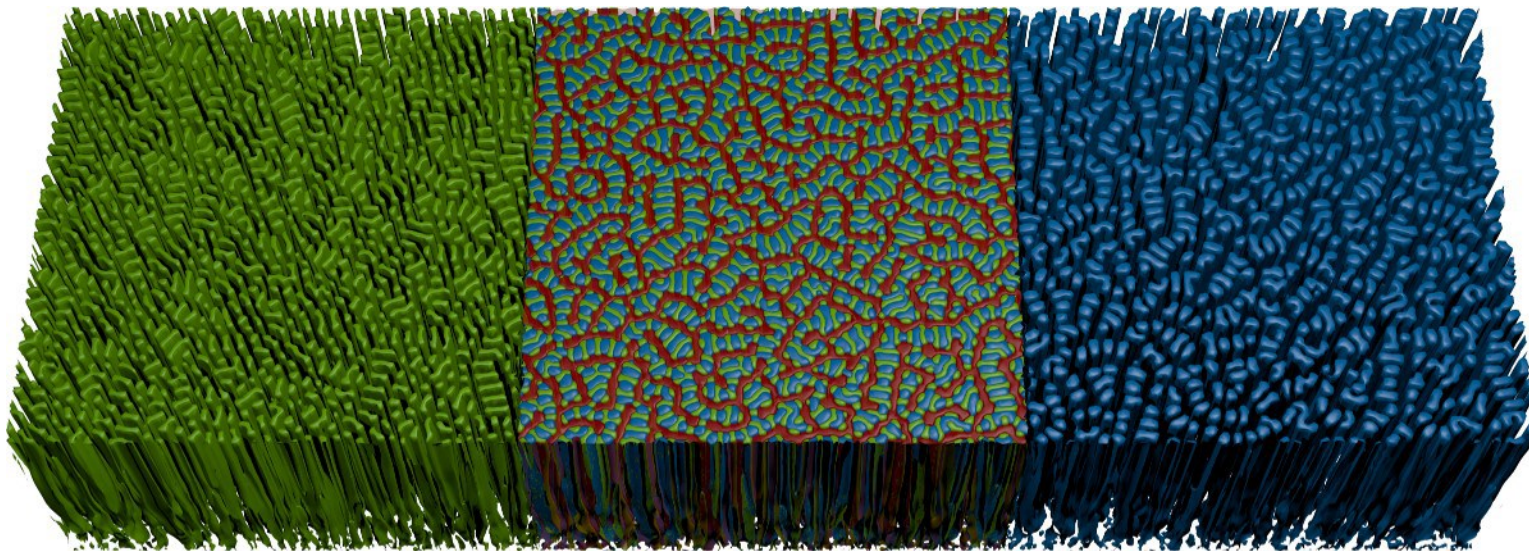
M. Bauer et al., *Massively Parallel Phase-Field Simulations for Ternary Eutectic Directional Solidification*, arXiv:1506.01684, accepted for SC15



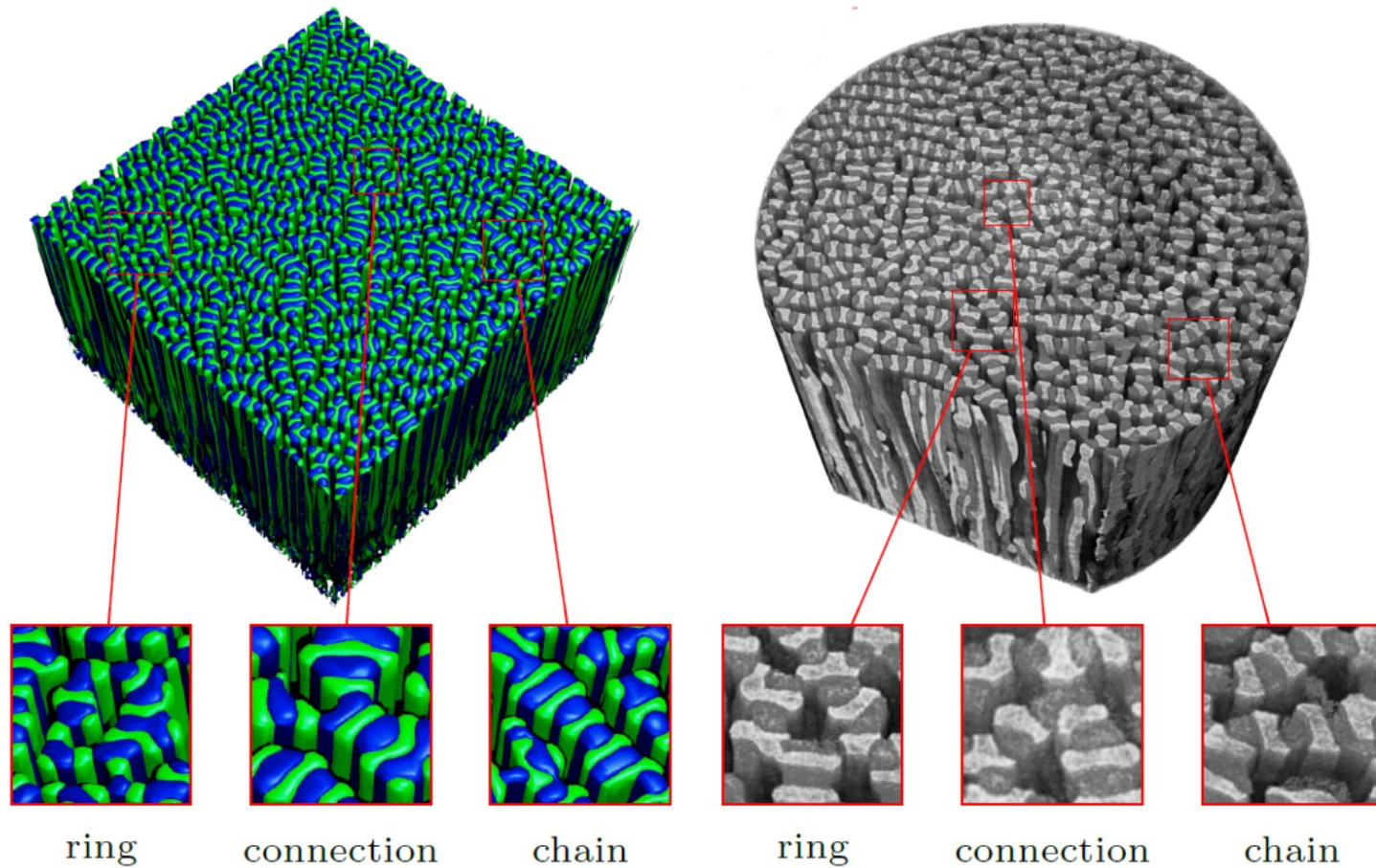
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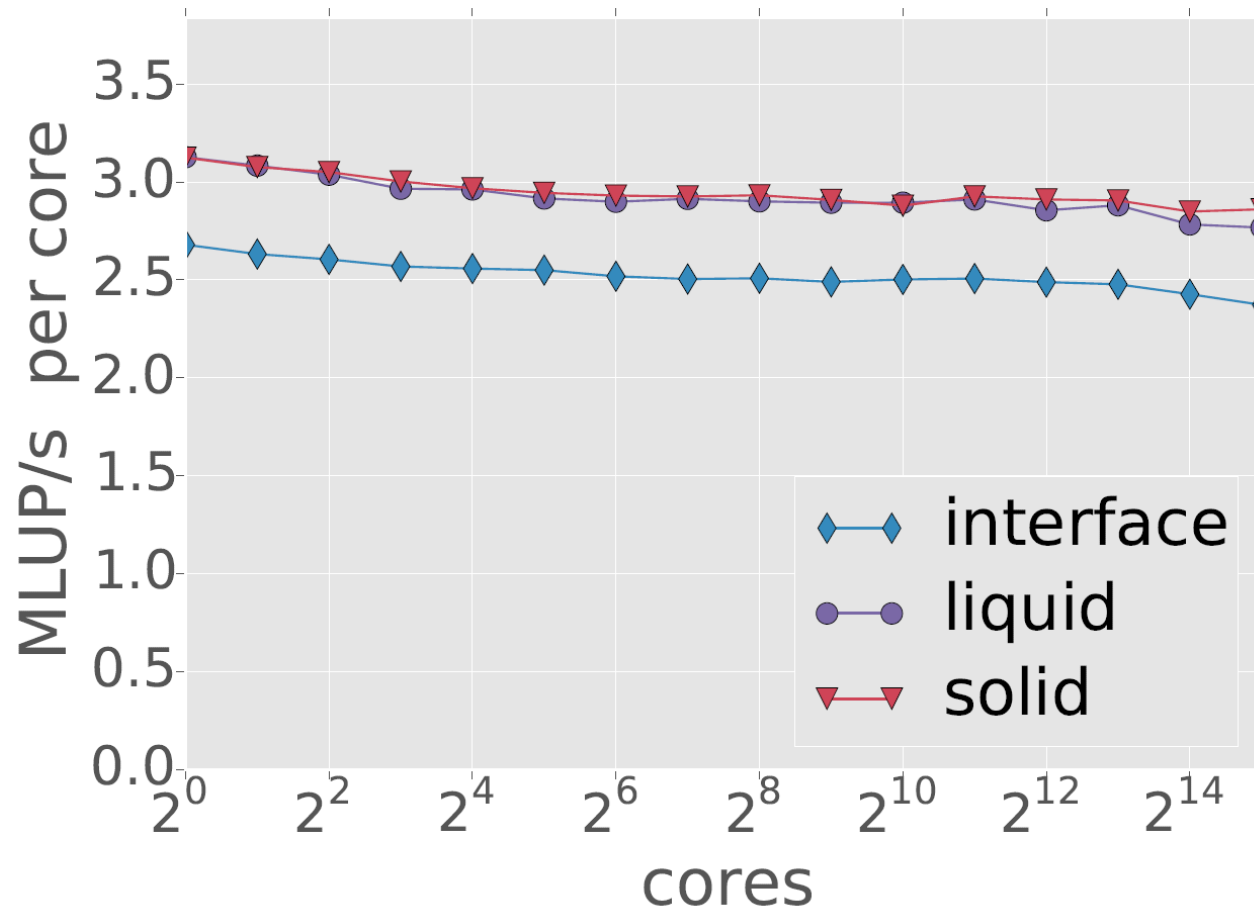
- Simulation of solidification of alloys (ternary eutectics)
- Simulates microstructure evolution
- Collaboration with material sciences group of Prof. Nestler, KIT



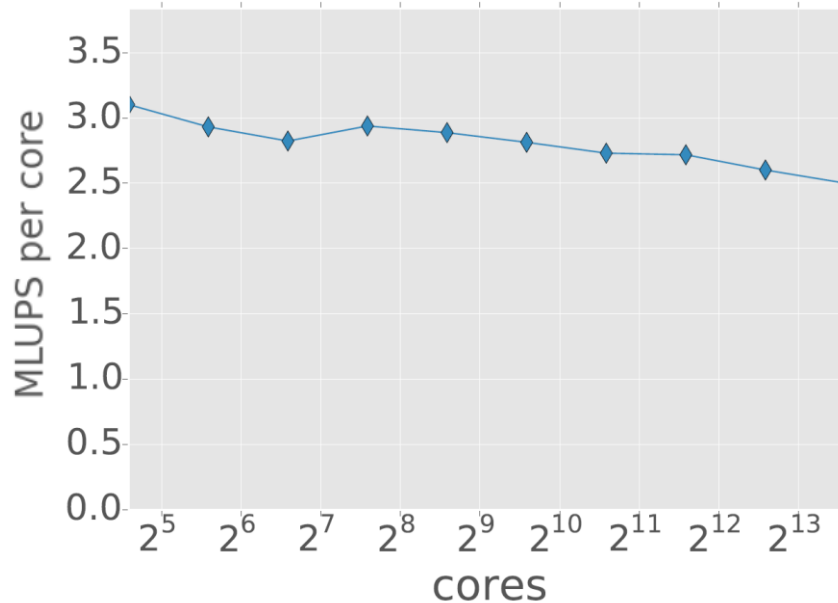
- Complex, **compute intensive** stencil code
- Ghost layer-based communication pattern (same as LBM)



simulation <-> experiment

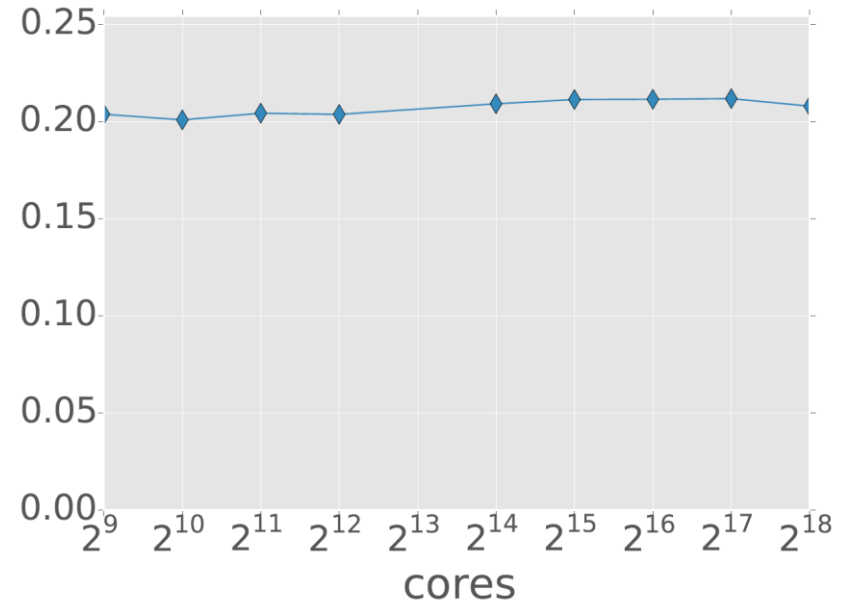


Weak scaling SuperMUC (Sandy Bridge)



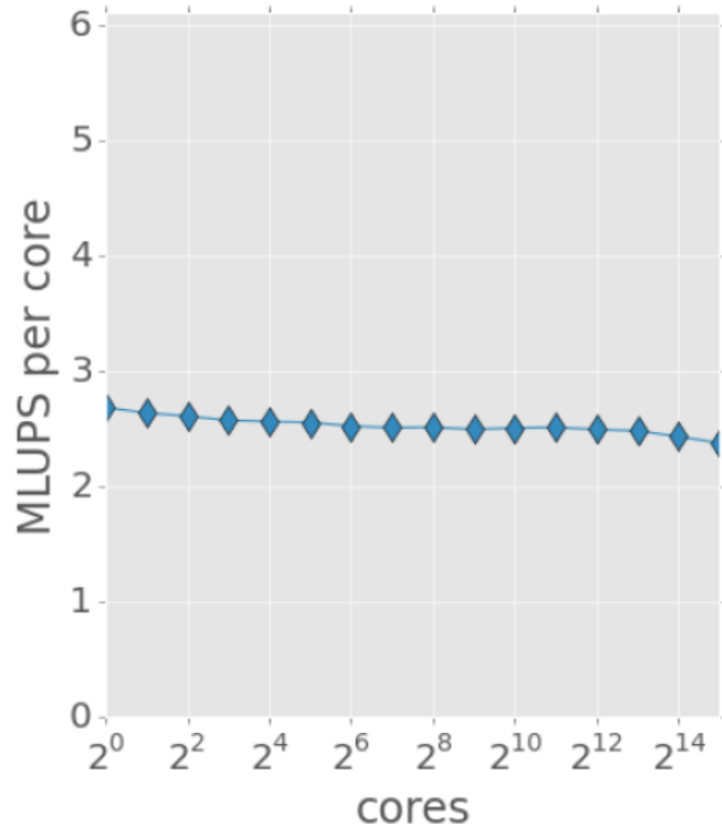
Hornet @ HLRS, Stuttgart

3944 nodes, 2x Intel Xeon E5-2680
24 cores / node (Haswell)
128 GiB RAM / node

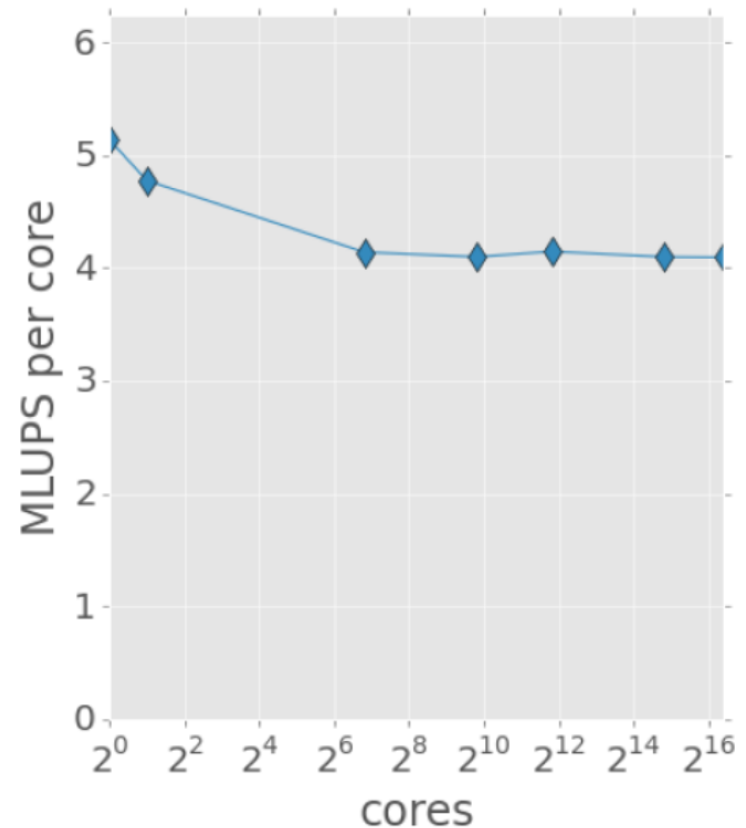


JUQUEEN

SuperMUC phase 1



SuperMUC phase 2



Comparison SuperMUC

Sandy Bridge <-> Haswell (3072 nodes, 2 x Intel Xeon E5-2697 w. 14 cores)



waLBerla, an ultra-scalable multi-physics simulation framework for piecewise regular grids

Christian Godenschwager - FAU Erlangen-Nürnberg - September 3, 2015



Outlook/Conclusion



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What we are currently working on:

- **LBM:**
 - Efficient data structures and compute kernels for sparse domains (indirect addressing scheme)
 - GPU/MIC support
- **Refinement:**
 - Dynamic, adaptive load balancing & refinement
- **Phase-field Methods:**
 - More models
 - GPU/MIC compute kernels

- waLBerla can achieve strong **single node performance** *and* excellent **scalability**
- It has an efficient implementation of **grid refinement** for LBM
- It is evolving from a pure LBM to a **multi-physics** framework



THANK YOU FOR YOUR ATTENTION!

QUESTIONS ?



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