

Workshop

Topic: **POWER Acceleration and Design Center: Opening Workshop**

Instructors: B. Anlauf (IBM), S. Baker (NVIDIA), P. Baumeister (JSC), M. Bühler (IBM), M. Bussmann (HZDR), A. Curioni (IBM), F. Delalondre (EPFL), M. Hamilton (NVIDIA), T. Hater (JSC), J. Kraus (NVIDIA), T. Lippert (JSC), F. Schifano (U Ferrara), D. Turek (IBM) and others

Contents: With different providers of HPC solution joining the OpenPOWER Foundation new opportunities for future supercomputer architectures emerge. To enable and improve their use by scientists and engineers that target the grand challenges facing society in the fields of energy and environment, information and healthcare, IBM, JSC and NVIDIA have set up the 'POWER Acceleration and Design Center' (PADC). With this workshop we will mark the inauguration of PADC and bring together experts from different areas to present initial experience on this new technology roadmap.

Time: Monday, 12 October 2015, 14:00, until
Tuesday, 13 October 2015, 15:30

Venue: Jülich Supercomputing Centre, building 16.4, room 301 (Rotunda)

Interested scientists are cordially invited to participate in this workshop. The number of participants is limited and we therefore ask for registration at: <https://indico-jsc.fz-juelich.de/e/PADC>

sgd Prof. Dr. Dirk Pleiter