HPC Optimisation and Scaling Workshop

Jülich Supercomputing Centre
18 – 22 February 2019

<u>Agenda</u>

Monday 18 February 2019

09:00 – 09:30	Welcome and Introduction to Workshop
09:30 – 10:00	Structured parallel performance engineering
10:00 – 12:00	*Introductory presentations of Scaler code-teams [10mins each]*
12:00 – 13:00	Lunch
13:00 – 15:00	***Execution characterisation*** with Intel Application Performance Snapshot (APS) ***and MPI correctness validation***
15:00 – 17:00	Hands-on with own code, submission of overnight jobs

Tuesday 19 February 2019

09:00 – 11:00	Review of overnight jobs & new large jobs; hands-on with own code
11:00 – 12:00	*Status presentations of Scaler code-teams [5mins each]*
12:00 – 13:00	Lunch
13:00 – 15:00	***Single-core performance analysis*** with Intel VTune & Advisor
15:00 – 17:00	Hands-on with own code, submission of overnight jobs

Wednesday 20 February 2019

09:00 – 11:00	Review of overnight jobs & new large jobs; hands-on with own code
11:00 – 12:00	*Status presentations of Scaler code-teams [5mins each]*
12:00 – 13:00	Lunch
13:00 – 15:00	***Multi-threading and node-level analysis*** with Intel VTune, Advisor & Inspector
15:00 – 17:00	Hands-on with own code, submission of overnight jobs

Thursday 21 February 2019

09:00 – 11:00	Review of overnight jobs & new large jobs; hands-on with own code
11:00 – 12:00	*Status presentations of Scaler code-teams [5mins each]*
12:00 – 13:00	Lunch
13:00 – 15:00	***Multi-node performance analysis*** with Scalasca/Score-P & Vampir
15:00 – 17:00	Hands-on with own code, submission of overnight jobs

Friday 22 February 2019

09:00 – 12:00	Review of overnight jobs & new large jobs; hands-on with own code; preparation of Scaler code-team final presentations
11:45 – 13:00	Lunch
13:00 – 15:00	*Final presentations of Scaler code-teams [10mins each]*
15:00 – 17:00	Wrap-up