



# SETTING SAIL THE JOURNEY OF NEUROMORPHIC COMPUTING

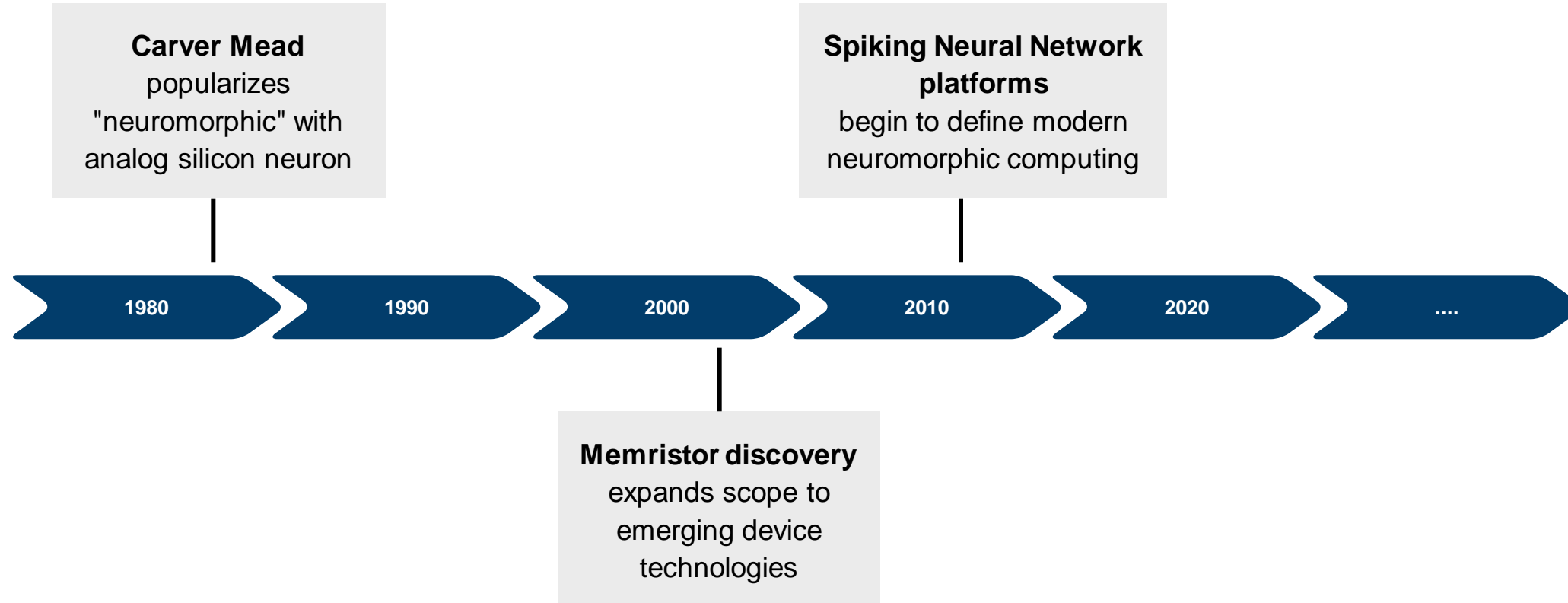
04.12.2024 | GEORGIA PSYCHOU, GUIDO TRENCH, SANDRA DIAZ, BORIS ORTH

# OUTLINE

- What is Neuromorphic Computing?
- From devices to applications: The neuromorphic landscape
- Local activities
- Outlook and future directions

# WHAT IS NEUROMORPHIC COMPUTING?

The term „Neuromorphic“ in time



# WHAT IS NEUROMORPHIC COMPUTING?

## Why look to the brain?

### Computing paradigms

#### Digital computing



- Excels in precise, repetitive tasks (e.g., large-scale calculations)
- Operates on predefined, rule-based processes (algorithmic steps, Turing model)
- Requires significant power (especially for tasks like AI model training, often on the order of Megawatts)

#### Neuromorphic computing



- Inspired by brain's biological principles: parallel processing, event-driven operations, energy-efficient adaptation
- Spiking Neural Networks (SNNs): A prominent approach within neuromorphic computing
  - Simple, parallel units (neurons)
  - Memory and processing co-located
  - Event-driven operations based on spikes
- Emulates brain-like computation: adaptability, real-time learning, energy efficiency

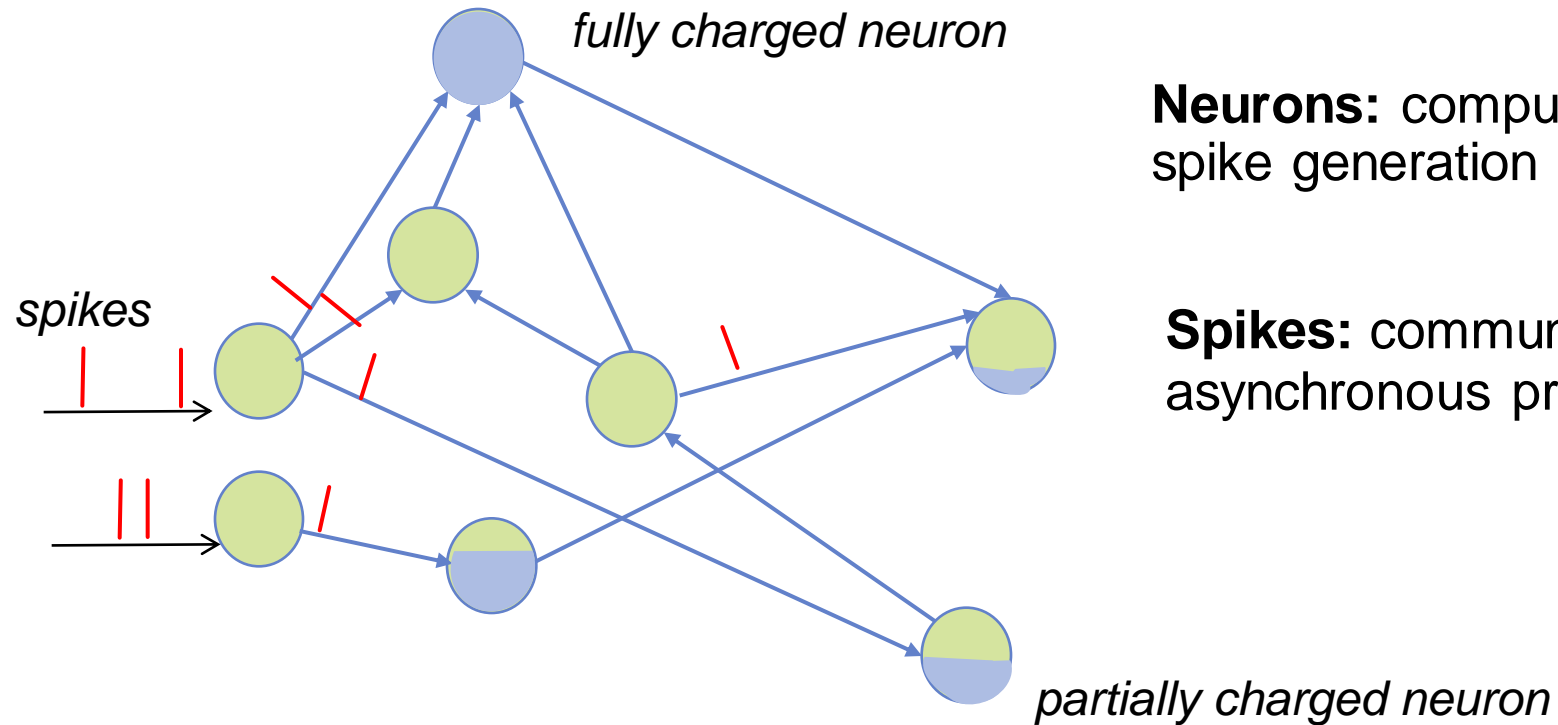
#### Brain computing



- Excels in pattern recognition, adaptability, and real-time decision-making
- Self-organizing neural computation, continuously interacting with and adapting to the environment
- Operates efficiently with low power (on the order of tens of Watts)

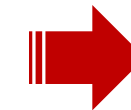
# WHAT IS NEUROMORPHIC COMPUTING?

## Spiking Neural Networks (SNNs): Constituent parts and challenges 1/2



**Neurons:** computation, spike generation

**Spikes:** communication, asynchronous propagation



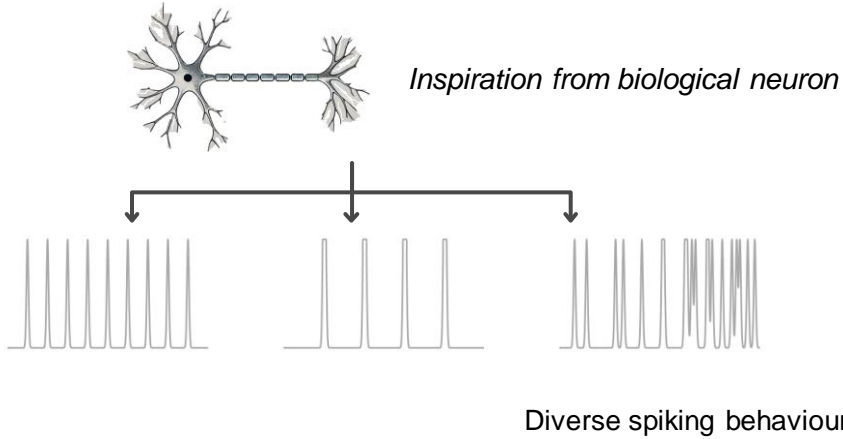
overall computation time-dependent

Inspired by Schuman et. al. (2022)

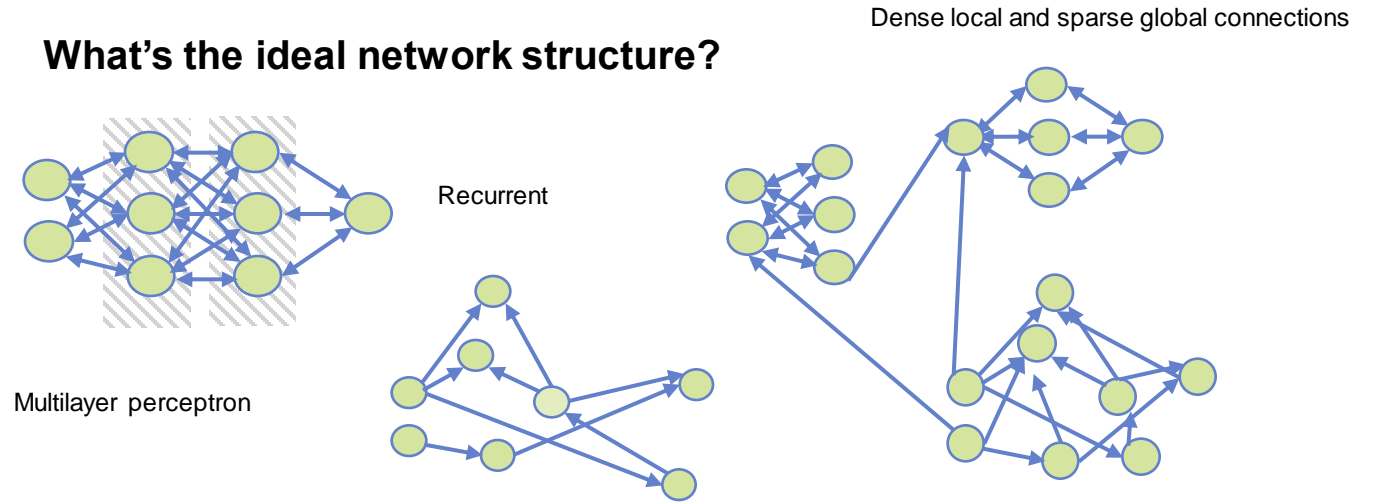
# WHAT IS NEUROMORPHIC COMPUTING?

## Spiking Neural Networks (SNNs): Constituent parts and challenges 2/2

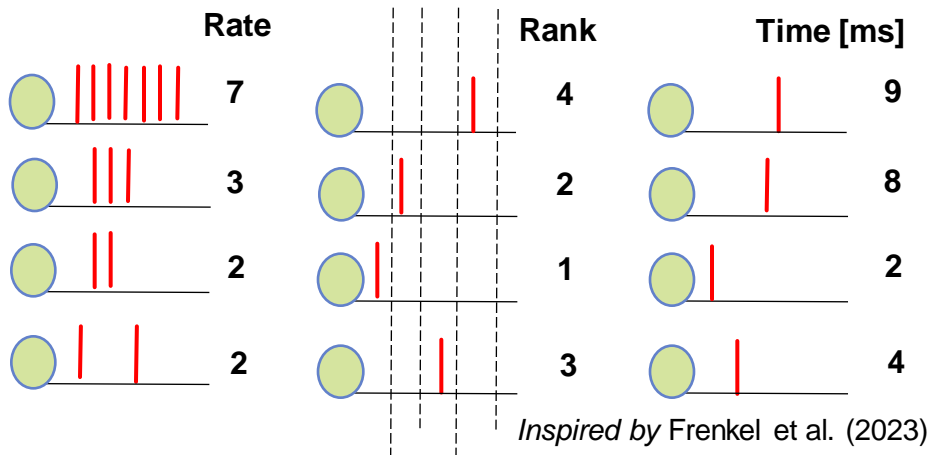
### Which neuron to choose?



### What's the ideal network structure?

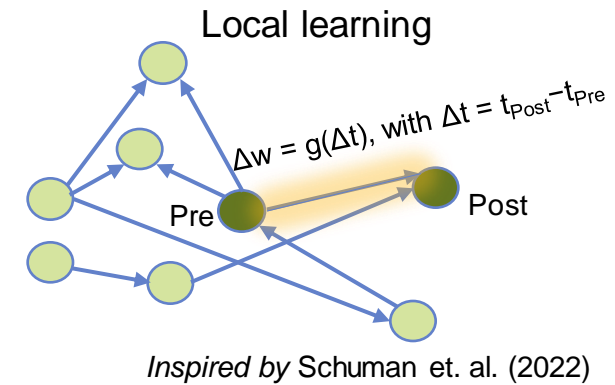
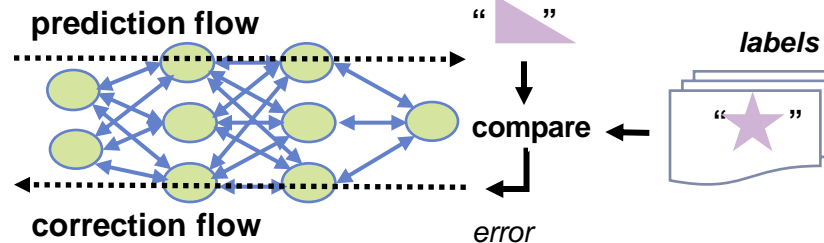


### How to encode information?

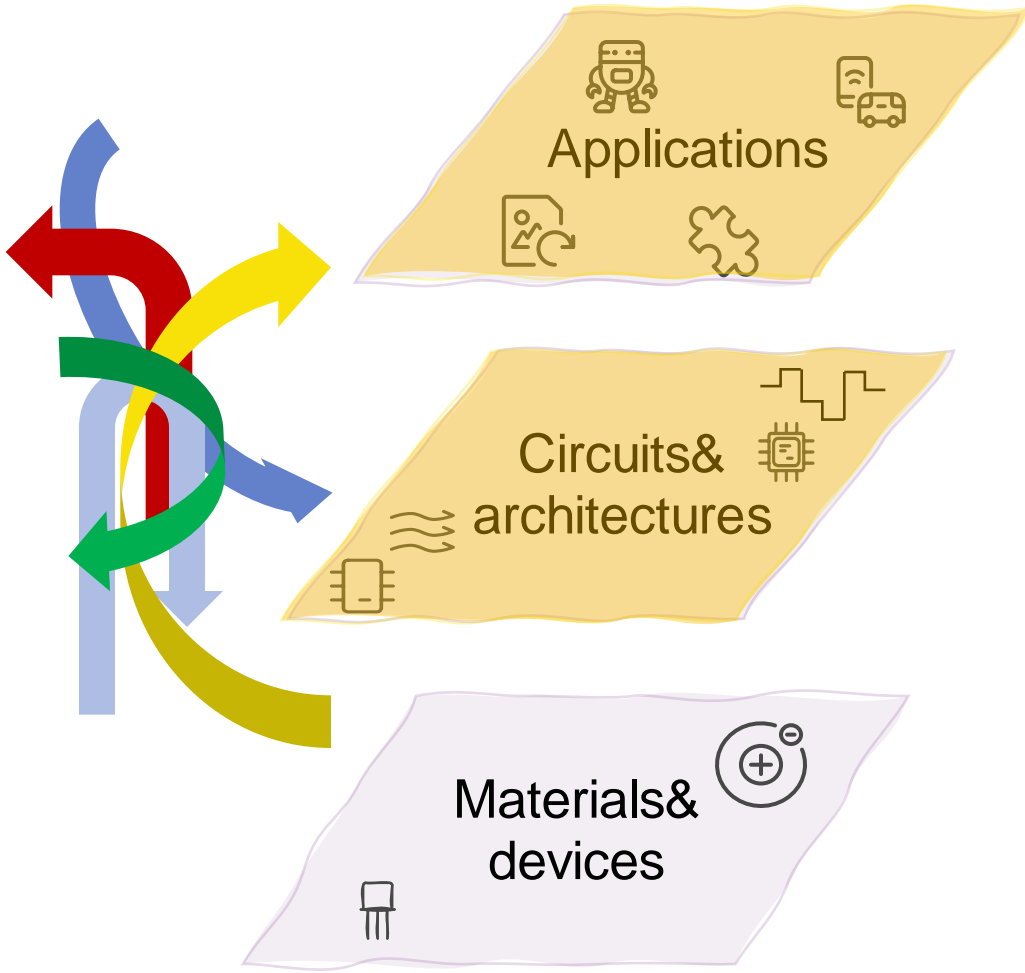
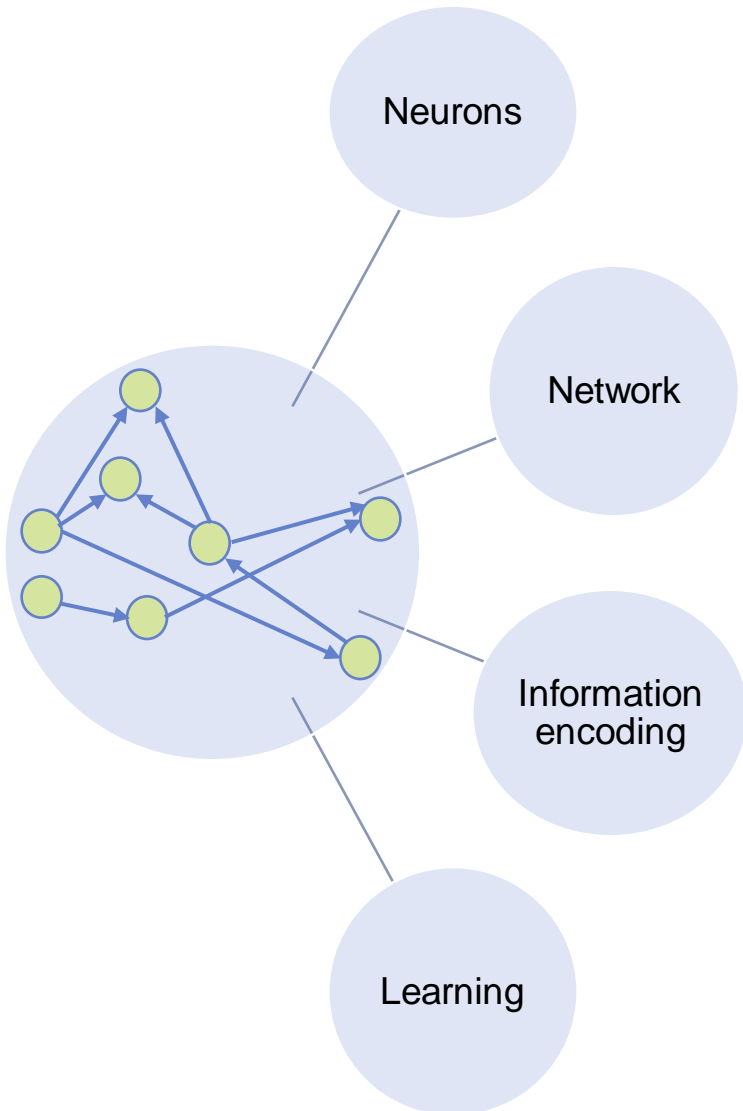


### Learning: go local or global?

- Global learning
- Backward or forward propagation?
  - Differentiability of spikes



# FROM DEVICES TO APPLICATIONS: THE NEUROMORPHIC LANDSCAPE



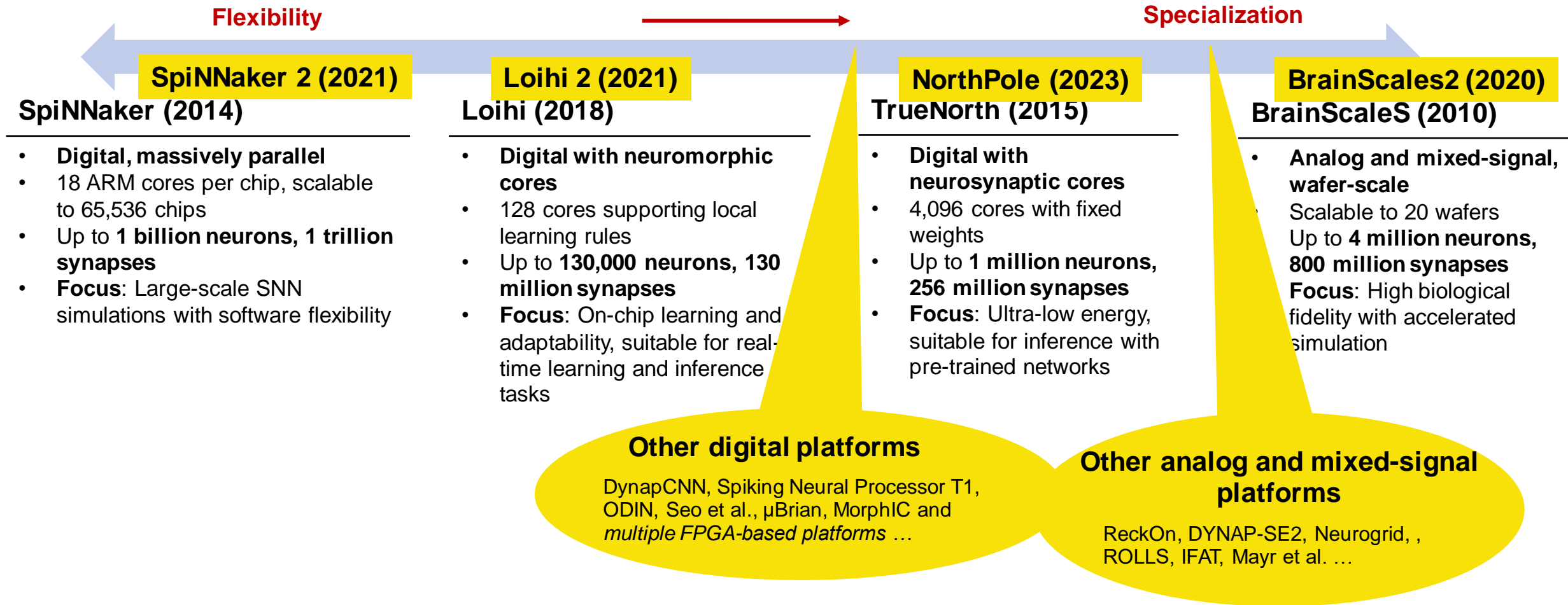
graph-based, optimization algorithms ...  
autonomous systems, robotics...

custom, programmable, digital, analog ...

optical devices, memristors ...  
phase-change, ferroelectric ...

# FROM DEVICES TO APPLICATIONS

## Neuromorphic experimentation platforms



- **Chips available for purchase in recent years:** *Speck* and *Xylo* by SynSense (2021), *Akida* by BrainChip (2020), and *neuromorphic cameras* (Prophesee, iniVation) (2023).

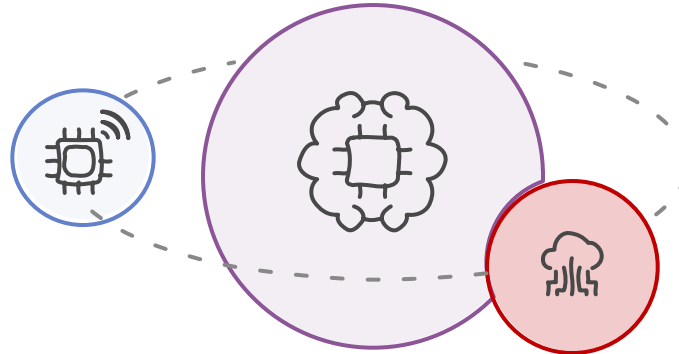


# FROM DEVICES TO APPLICATIONS

## Exploring neuromorphic applications

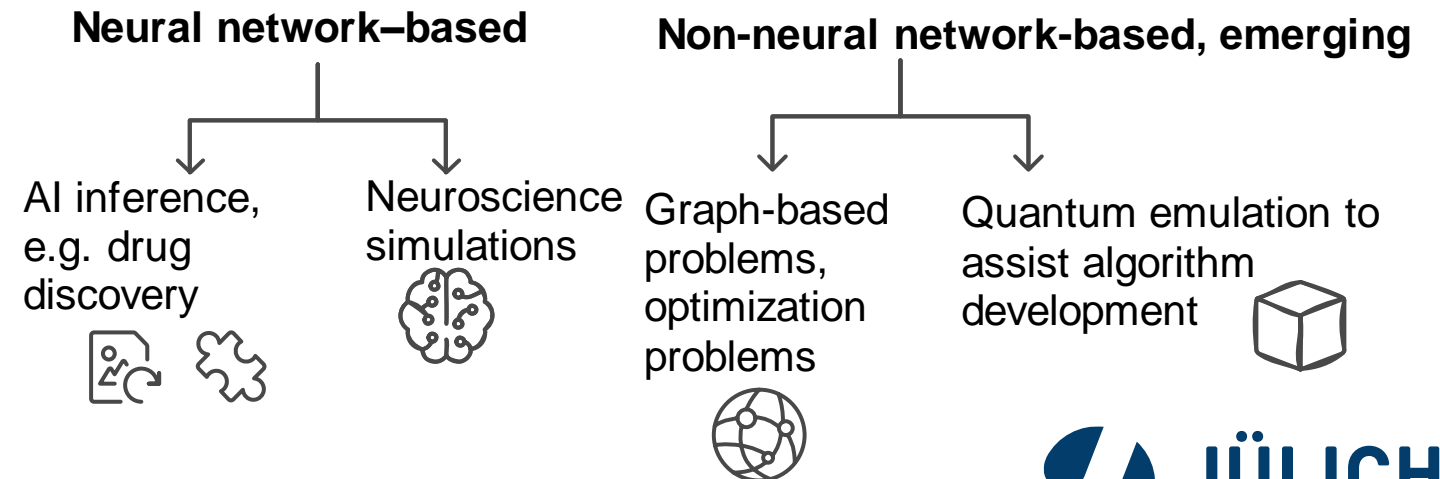
### Real time, adaptive tasks

- Decentralized, resource-constrained, real-time tasks
- Event-driven processing with online learning for real-time adaptation
- Ideal for robotics, wearables, IoT devices, and autonomous systems



### Computationally intensive tasks

- Large-scale, resource-demanding tasks
- Significant computational demands combined with energy-efficient processing
- Neuromorphic efficiency to address problems that are resource-intensive for traditional systems

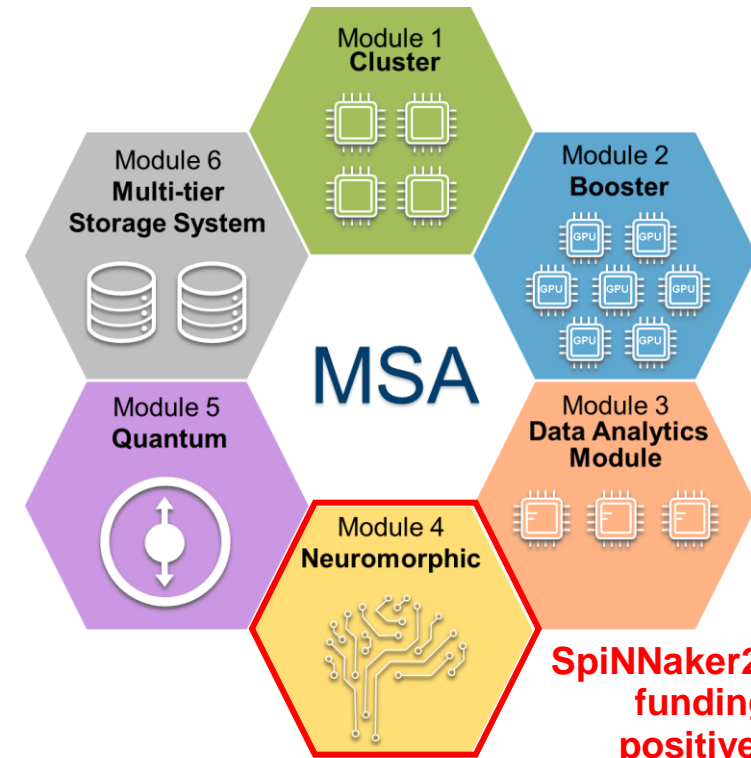


# LOCAL ACTIVITIES: NEUROMORPHIC COMPUTING AT JSC

## Modular Supercomputing Architecture (MSA) workflows

Integrating transformative technologies (Quantum Computing [QC] and Neuromorphic Computing [NC]) into HPC workflows using MSA. Examples :

- **Emulating quantum:** Perform QC emulation on NC module, validate on QC hardware.
- **Brain simulations:** Run neuroscience simulations on NC module with pre- and post-processing on cluster module.
- **Synthetic brain data generation:** NC module produces structural and functional brain data for training foundation models on booster module.
- **AI workflows:** Cluster and booster modules handle data-intensive training; NC module provides energy-efficient inference.



- **Suarez** et al. "Modular Supercomputing Architecture – A Success Story of European R&D", ETP4HPC White Paper. (2022) Available at <https://www.etp4hpc.eu/white-papers.html#msa>.
- **Suarez** et al., "Modular Supercomputing Architecture: from idea to production", Chapter 9 in Contemporary High Performance Computing: from Petascale toward Exascale, Volume 3, p 223-251, CRC Press. (2019)

# LOCAL ACTIVITIES: NEUROMORPHIC COMPUTING AT FZJ

## Jülich Neuromorphic Computing Alliance (JUNCA)

**Theory and modeling** to understand dynamics and **computation in the brain**

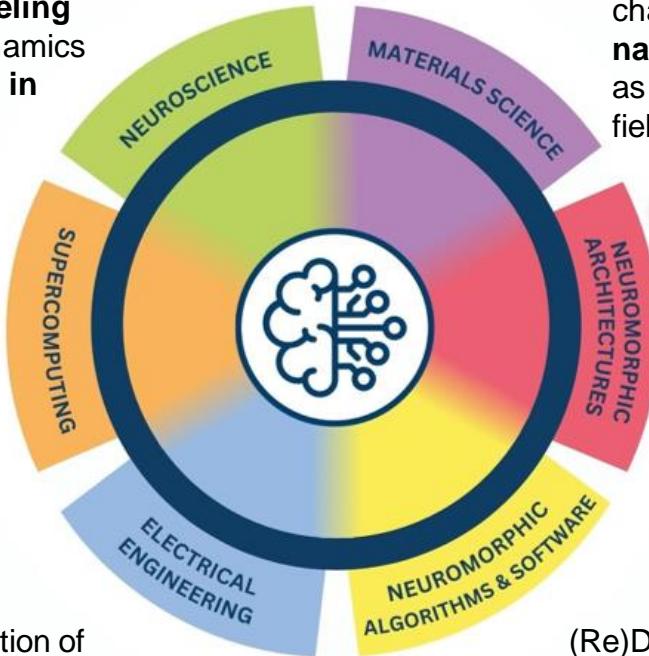
IAS-6

Development and integration of **High-performance computing architectures** and tools for **brain research** and **AI**

JSC

Conception and implementation of physical-mathematical neuromorphic principles in special **integrated circuits and prototypes**

ZEA-2



Design, fabrication and characterization of **nanoelectronic elements** such as memristors and nanowire field-effect transistors

PGI-7

PGI-9

PGI-10

**Co-design** and engineering of **brain-inspired hardware** for challenging computational problems

PGI-14

(Re)Design of **computing algorithms** and computer **architectures** from the perspective of neuroscience

PGI-15

## FACTS AND FIGURES



**Resources:**

> 13 institute divisions contribute to the field



**Budget:**

59 million €  
(institutional: 19 million €, third-party funds: 40 mio. € in 2023)



**PhD students:**

63  
(directly supervised by scientists of the institute divisions in 2023)



**Publications:**

more than 80 in 2023

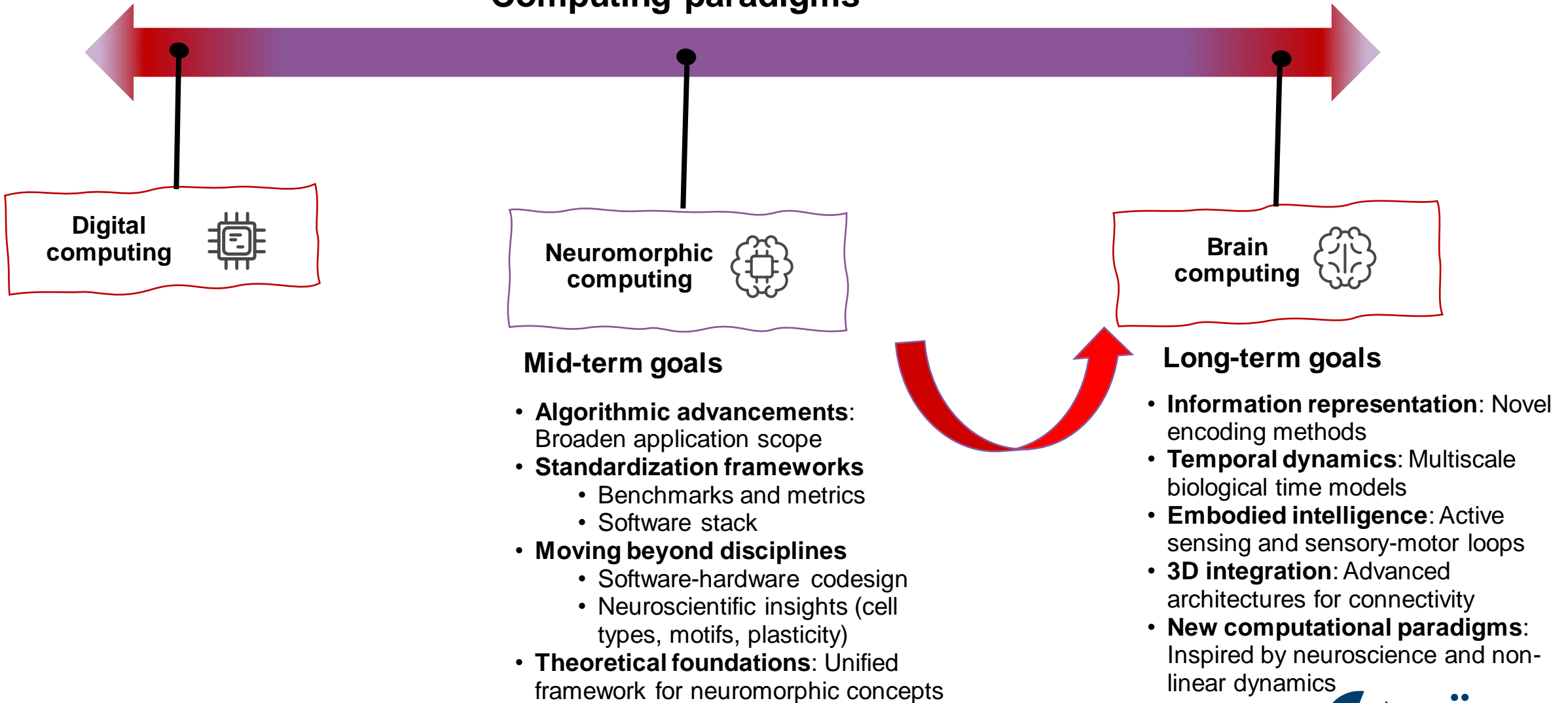


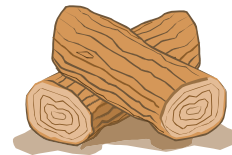
**Employees:**

192  
(third-party funded: 60 in 2023)

# SAILING INTO THE FUTURE

## Computing paradigms





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- **Schuman et al. (2022)**. Opportunities for Neuromorphic Computing Algorithms and Applications. *Nature Computational Science*, 2(1): 10–19.
- **Kurenkov (2020)**. A Brief History of Neural Nets and Deep Learning. *Skynet Today*.
- **Akopyan et al. (2015)**. Truenorth: Design and Tool Flow of a 65 MW, 1 Million Neuron Programmable Neurosynaptic Chip. *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, 34(10): 1537-1557.
- **Davies et al. (2018)**. Loihi: A Neuromorphic Manycore Processor with On-Chip Learning. *IEEE Micro*, 38(1): 82-99.
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- **Open Neuromorphic (2024)**. Neuromorphic Computing. Available at: <https://open-neuromorphic.org/neuromorphic-computing/> [Accessed: December 3, 2024].